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Model JQE 36–3 (M)HS

Serial No. H7159.4....

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INSTRUCTION MANUAL

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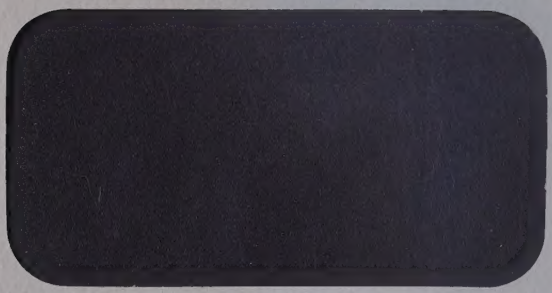

KEPCO

JQE

design group

COMPACT

POWER SUPPLY

Model JQE 36–3 (M)HS

Serial No. H7159.4

INSTRUCTION MANUAL

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KEPCO JQE–HS SERIES
HIGH–SPEED OPERATIONAL POWER SUPPLIES

The major part of this instruction manual (Sections,I through VI) is applicable for both, the Kepco Series JQE and JQE–HS Power Supplies. The addendum (See Appendix), containing additional operating and test instructions, is applicable for Series JQE–HS (High-Speed) models only.

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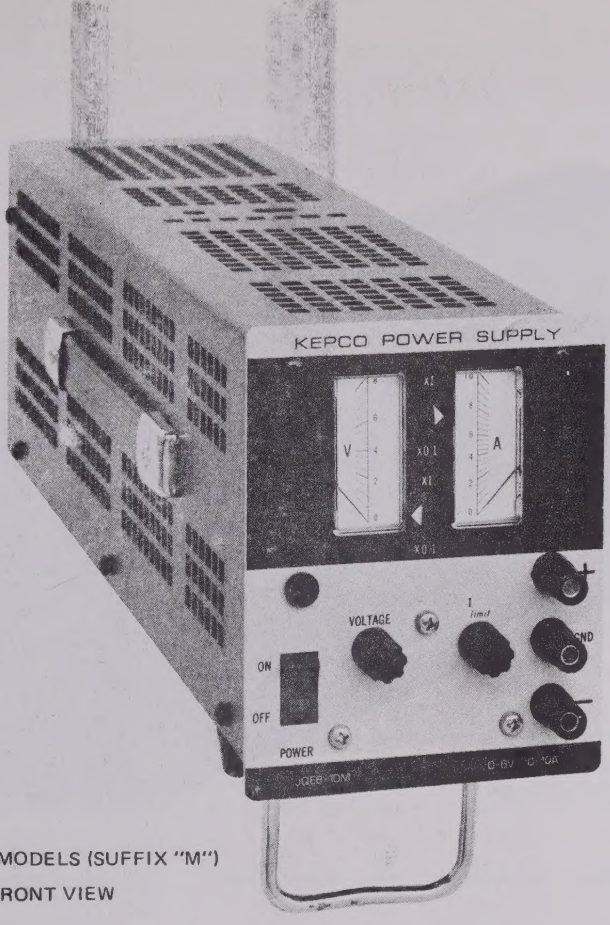
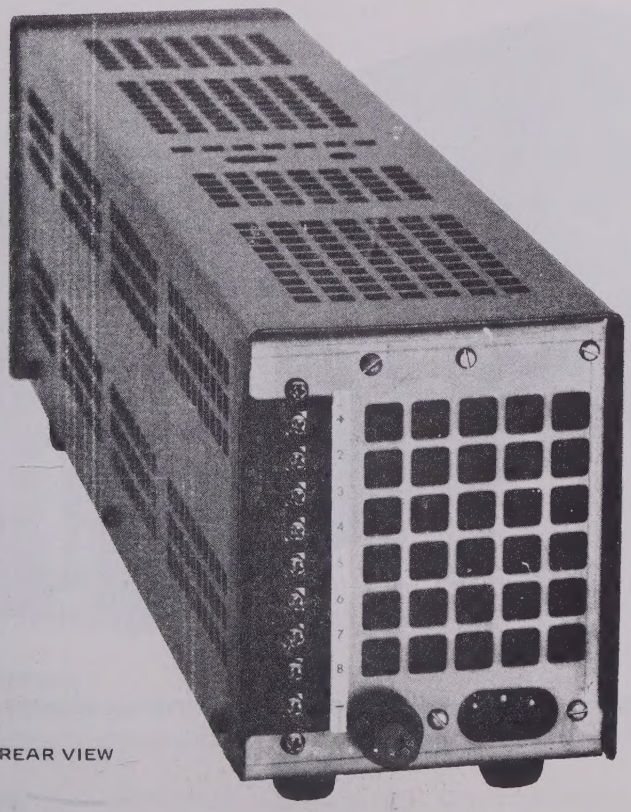
*FIG. 6-3 on Models with suffix "–VP" only.

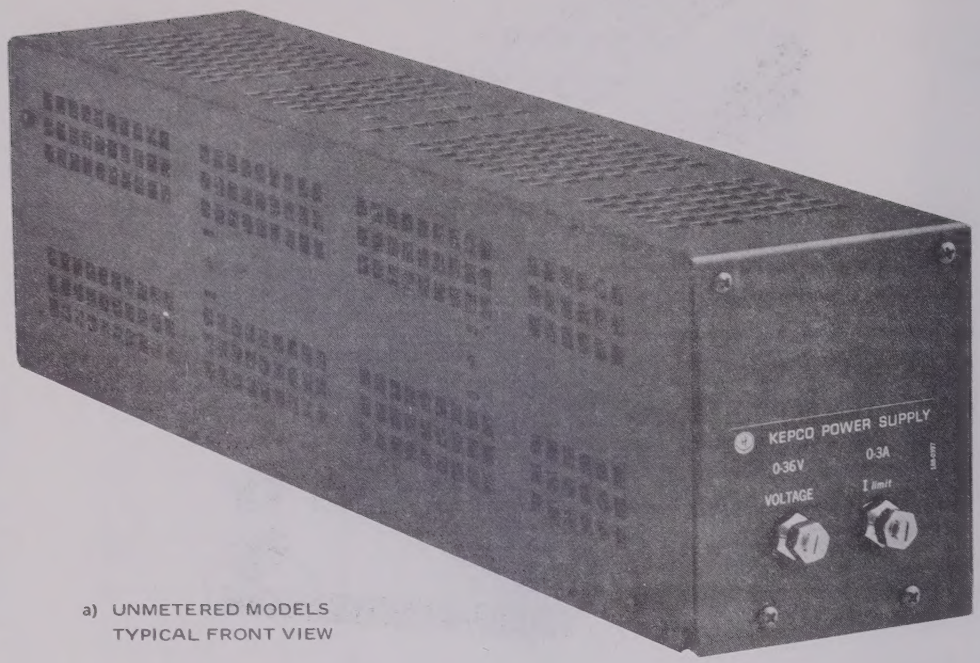


a) METERED MODELS (SUFFIX "M")
TYPICAL FRONT VIEW

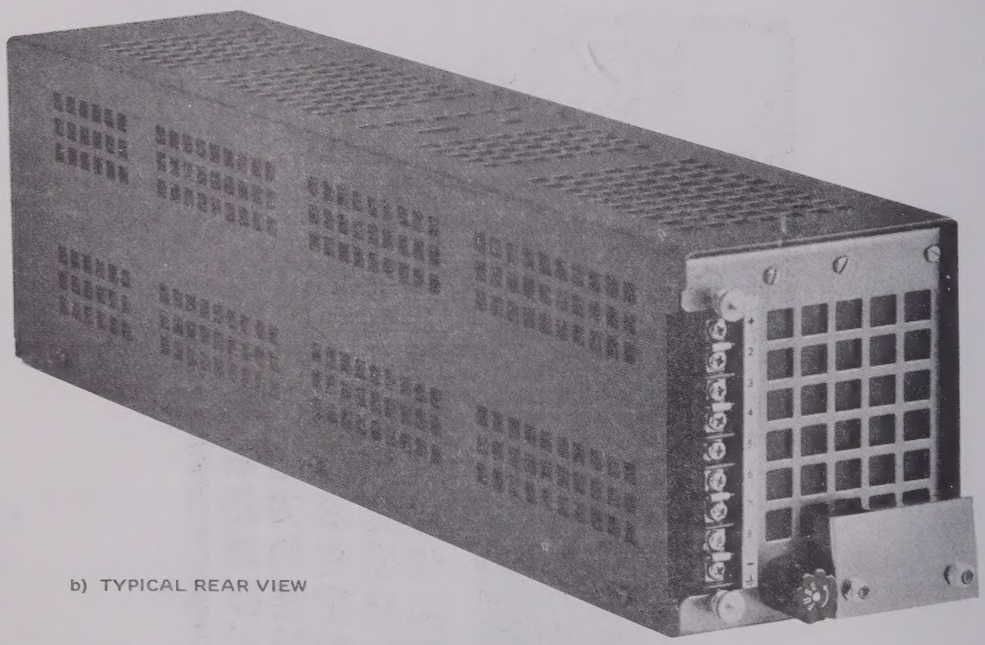


b) TYPICAL REAR VIEW

FIG. 1-1 JQE QUARTER-RACK POWER SUPPLY, TYPICAL (METERED)



a) UNMETERED MODELS
TYPICAL FRONT VIEW



b) TYPICAL REAR VIEW

FIG. 1-2 JQE QUARTER-RACK POWER SUPPLY, TYPICAL (UNMETERED)

SECTION I – INTRODUCTION

1-1 SCOPE OF MANUAL

1-2 This manual contains instructions for the installation, operation and maintenance of the Kepco JQE "QUARTER-RACK" Series of Power Supplies.

1-3 GENERAL DESCRIPTION

1-4 Kepco Series JQE Power Supplies are general purpose, precision regulated voltage sources in the "quarter-rack" configuration. JQE Power Supplies have linear, full-dissipation series-regulators (NPN) driven by an integrated circuit operational amplifier. A sharp current-limit circuit renders the power supply completely short-circuit proof. JQE Power Supplies may be readily converted to provide constant current by the addition of an external sensing resistor and a "current control". The output of the JQE Power Supply is completely programmable. All necessary connections are available on terminals at the rear barrier-strip.

1-5 The compact design of the Kepco JQE "QUARTER-RACK" Series was mad possible by a unique heatsink design of exceptional efficiency in combination with highly reliable low-noise fans. JQE power supplies feature all-silicon design with conservatively rated components for added reliability.

1-6 JQE Power Supplies are identical in their mechanical dimensions (Refer to FIG. 1-3) and in their electrical specifications, except as noted in TABLE 1-1.

1-7 OPTIONS. Optional features of Kepco Power Supplies are indicated by a letter following the model designation:

a) JQE Power Supplies with suffix "E" are equipped with an "E_{io} Null" adjustment, which allows exact zero calibration for precision programming applications.

b) JQE Power Supplies with suffix "HS" are optimized for high speed programming. For these models, a special addendum containing all supplementary specifications is provided in the appendix of the manual. The "E_{io} Null" ("E") option is automatically included with the "HS" option.

c) JQE Power Supplies with suffix "M" are equipped with two dual range panel meters which permit simultaneous monitoring of the output voltage and output current. Meter range-switches allow for full scale reading of either 10% or 100% of the voltage and current outputs.

d) JQE Power Supplies with suffix "T" have special references which improve the temperature coefficient of the supply (see paragraph 1-11 for specification).

e) JQE Power Supplies with suffix "VP" are equipped with an overvoltage protector. This electrical "crowbar" shorts the output through a silicon controlled rectifier (SCR) if the output voltage exceeds a preadjustable limit on the protector (see paragraph 1-12 for specifications).

1-8 The main chassis frame of the power supply is constructed from cold-rolled steel, as is the perforated wrap-around cover. Front panel material is aluminum (Refer to FIG. 1-3 for finish).

1-9 ACCESSORIES

a) RACK ADAPTER (FOR FOUR UNITS): KEPCO MODEL RA24. Fits standard EIA rack dimensions. FILLER PANELS to cover empty slots if adapter is used for less than its capacity:

1) KEPCO MODEL RFP 24–1, to cover one (1) empty slot.
2) KEPCO MODEL RFP 24–2, to cover two (2) empty slots.
3) KEPCO MODEL RFP 24–3, to cover three (3) empty slots.

1-10 SPECIFICATIONS, GENERAL

a) AC INPUT: 105 to 125V AC or 210 to 250V AC (selectable, refer to SECTION II), 50 to 65 Hz,* single phase. Refer to the table of general specifications (TABLE 1-1) for the AC input current for each model.

b) OPERATING TEMPERATURE RANGE: (–)20°C to (+)71°C (without derating of the output).

*Consult factory for operation on power line frequencies above 65 Hz.

c) STORAGE TEMPERATURE: –50°C to (+)85°C.

d) COOLING; High efficiency, single bearing fan, (permanently lubricated) with special low noise non-metal blade.

e) ISOLATION: A maximum of 500 volts (DC or p-p) can be connected between chassis and either output terminal.

f) OUTPUT IMPEDANCE: TABLE 1-1.

MODEL	DC OUTPUT RANGE		OUTPUT IMPEDANCE (OHMS + μH)			MAX. INPUT CURRENT
	VOLTS	AMPS	DC–100 Hz	0.1–1 kHz	1 kHz–100 kHz	Amps @ 125V AC
JQE 6–10M	0–6	0–10	30 $\mu\Omega$	0.01	0.02 + 1	2.3
JQE 15–6M	0–15	0–6	125 $\mu\Omega$	0.01	0.02 + 1	2.3
JQE 25–4M	0–25	0–4	300 $\mu\Omega$	0.01	0.02 + 1	2.3
JQE 36–3M	0–36	0–3	600 $\mu\Omega$	0.01	0.02 + 1	2.3
JQE 55–2M	0–55	0–2	1.4 mΩ	0.01	0.02 + 1	2.3
JQE 75–1.5M	0–75	0–1.5	2.5 mΩ	0.01	0.02 + 1	2.3
JQE 100–1M	0–100	0–1	5 mΩ	0.01	0.02 + 1	2.3

TABLE 1-1 JQE GROUP, GENERAL SPECIFICATIONS

1-11 SPECIFICATIONS, ELECTRICAL

a) Refer to TABLE 1-2.

b) VOLTAGE RECOVERY TIME: (for step load current), <50 μsec.

c) OVERSHOOT: No output voltage overshoot from turn-on, turn-off, or power failure for output settings above 25% of maximum rated output voltage. Below 25%, output voltage overshoot is a function of load current and is negligible for loads in excess of 10% of the maximum rated load current.

PARAMETER	VOLTAGE MODE	CURRENT MODE (external sensing)
OUTPUT RANGE (Minimum)	0 to 100% E_o max.	1 mA to 100% I_o max.
REGULATION, LINE (105 to 125V or 210 to 230V AC)	< 0.0005% of E_o max.	< 0.005% of I_o max.***
REGULATION, LOAD (FROM "NO LOAD" TO "FULL LOAD")	< 0.005% of E_o max. or 0.2 mV*	< 0.01% of I_o max.***
STABILITY: (8 Hr. TIME PERIOD AT CONSTANT LOAD-LINE – TEMPERATURE)	< 0.01% of E_o max. or 1 mV*	< 0.02% of I_o max.***
TEMPERATURE COEFFICIENT per °C**	< 0.01% of E_o max.	< 0.02% of I_o max.***
RIPPLE: rms, (MAXIMUM UNDER ANY OPERATING CONDITION)	< 0.2 mV	< 0.02% of I_o max.

*NOTE: whichever is greater.

**NOTE: Models with suffix "T" have Temperature Coefficients of 0.005% per° C (Voltage Mode) and 0.01% per ° C (Current Mode).

***NOTE: Current Regulation, Stability, and Temperature Coefficient is measured across the external sensing resistor. This resistor must be a high quality, wire-wound unit, with a wattage at least 10 times the actual power dissipated, have a Temperature Coefficient of at least 20 parts per million, and produce a voltage sample of 1 volt (for I_o >5A) at the maximum rated output current.

TABLE 1-2 JQE ¼-RACK ELECTRICAL SPECIFICATIONS

1-12 SPECIFICATIONS,PERFORMANCE

a) VOLTAGE CONTROL*: 10 turn precision rheostat at the front panel, resolution: 0.05% of E_O max. Controls the output voltage from zero to $E_O$max.

b) CURRENT LIMIT CONTROL*: 10 turn vernier control at the front panel permits adjustment of the current limit from 10% to 105% of maximum rated output current (I_O).

c) REMOTE ERROR SENSING: Rear barrier-strip terminals provide for connection of the error sensing leads directly at the load, thus compensating for losses due to load current voltage drops. Up to 0.5V per output lead can be compensated.

d) REMOTE PROGRAMMING: Rear barrier termainals provide for remote control of the power supply by resistance (control loop uses 1 mA with a Programming Ration of 1000 ohms per volt), or by external control voltage.

e) SERIES/PARALLEL: Series operation of JQE Power Supplies is possible up to the rated isolation voltage. Parallel operation using "Automatic Parallel" or "Slave-Master" parallel connections may be performed.

f) OVERVOLTAGE PROTECTOR (MODELS WITH SUFFIX "VP")
   1) RANGE: 3V DC – 110V DC (adjustable).
   2) TRIGGERING TIME: 5 to 10 mircoseconds, adjustable by means of a delay-control.
   3) THRESHOLD: Overvoltage device may be set to within 5% or 0.25 volts of the power supply's output voltage (whichever is greater).

*Knob controlled on all metered models (suffix "M"), locking type screw-driver controls on unmetered models.

1-13 SPECIFICATIONS, PHYSICAL (Refer to FIG. 1-3 MECHANICAL OUTLINE DRAWING).

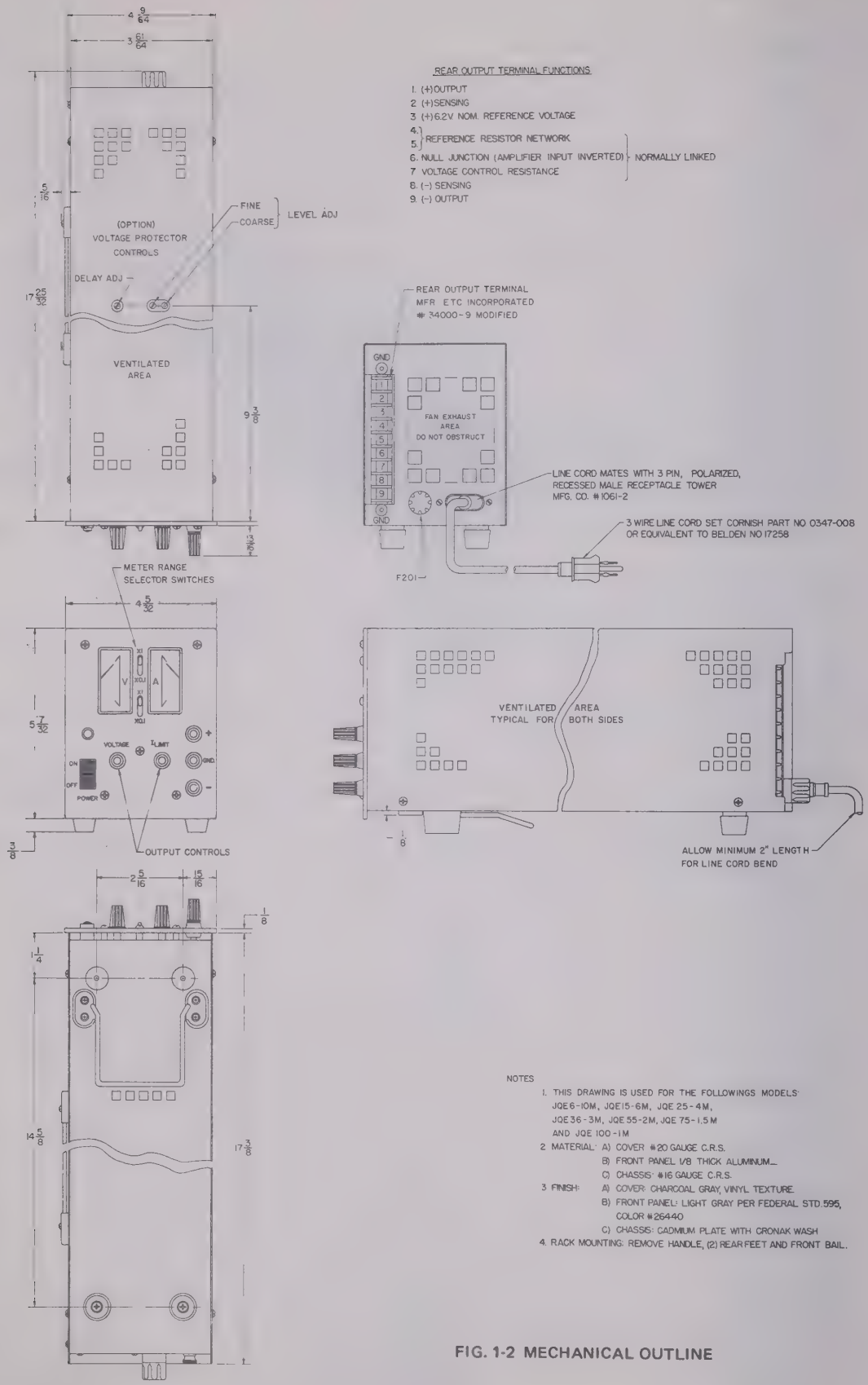
a) DIMENSIONS: (cased unit) Refer to Mechanical Outline Drawing (FIG. 1-3).

b) TERMINATIONS: Refer to Mechanical Outline Drawing (FIG. 1-3) and Section II of this Manual.

c) FINISH:
   1) CHASSIS: cadmium plated, cronak wash.
   2) PANEL (on models with suffix "M" only): Light gray, Color #26440, Federal Standard 595.
   3) CASE: Charcoal gray texture.

d) METERS (on model with suffix "M" only): Two dual range (100% and 10% of E_O max. and I_O max.), 2 inch tautband, recessed panel meters accuracy 3% of full scale reading, for output voltage and output current monitoring.

e) MOUNTING: Rack adaptor available, see "Accessories" (paragraph 1-9). A retractable "bail is provided to adjust the supply to a convenient viewing and operating angle on the bench. (Models with suffix "M" only).



NOTES

1. THIS DRAWING IS USED FOR THE FOLLOWINGS MODELS:
   JQE 6-10M, JQE 15-6M, JQE 25-4M,
   JQE 36-3M, JQE 55-2M, JQE 75-1.5M
   AND JQE 100-1M
2. MATERIAL: A) COVER #20 GAUGE C.R.S.
   B) FRONT PANEL 1/8 THICK ALUMINUM
   C) CHASSIS: #16 GAUGE C.R.S.
3. FINISH: A) COVER: CHARCOAL GRAY, VINYL TEXTURE
   B) FRONT PANEL: LIGHT GRAY PER FEDERAL STD. 595,
   COLOR #26440
   C) CHASSIS: CADMIUM PLATE WITH CRONAK WASH
4. RACK MOUNTING: REMOVE HANDLE, (2) REAR FEET AND FRONT BAIL.

FIG. 1-2 MECHANICAL OUTLINE

SECTION II – INSTALLATION

2-1 UNPACKING AND INSPECTION

2-2 This instrument has been thoroughly inspected and tested prior to packing and is ready for operation. After careful unpacking, inspect for shipping damage before attempting to operate. Perform the preliminary operational check as outlined in paragraph 2-11 below. If any indication of damage is found, file an immediate claim with the responsible transport service.

2-3 TERMINATIONS

a) FRONT PANEL: Refer to FIG. 2-2 and TABLE 2-2.
b) REAR: Refer to FIG. 2-3 and TABLE 2-3.
c) INTERNAL ADJUSTMENTS AND CALIBRATIONS: Refer to FIG. 2-1 and TABLE 2-1.

REFERENCE DESIGNATION	CONTROL	PURPOSE	ADJUSTMENT PROCEDURE
C403	Delay adjust*	Adjust Delay Time of VP.	
R19	I_O max. adjust	Current Limit Control Cal.	par. 5-6
R4	I_b adjust	Control Current Calibration	par. 3-16
R16	Lag Adjust (Not used on all models)	AC Stability Adjustment	par. 5-7
R14	E_{io} null adjust*	Output Voltage Precision Zero	par. 3-16
R412	Overvoltage Level adjust* (FINE)	Adjusts Protector Threshold	FIG. 6-3
R413	Overvoltage Level adjust* (COARSE)	Adjusts Protector Threshold	FIG. 6-3

***NOTE:** Optional controls, see par. 1-7

TABLE 2-1 INTERNAL ADJUSTMENTS AND CALIBRATIONS

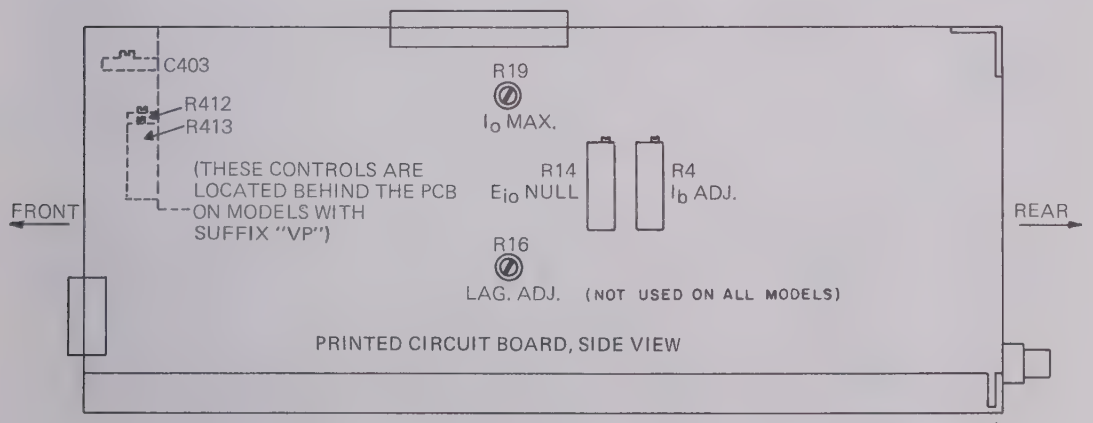


FIG. 2-1 LOCATION OF INTERNAL ADJUSTMENTS

2-4 AC INPUT REQUIREMENTS

2-5 This power supply is normally supplied for operation on a single phase, nominal 115V AC line. For conversion to 230V AC line operation, refer to FIG. 2-2. Remove the two wire jumpers between transformer terminals (1)—(2)—(3) and (4)—(5). Re-connect one (1) jumper between terminals (3) and (4). Do not change any other wiring on the transformer. Change main fuse value (F201) to 1/2 its former rating.

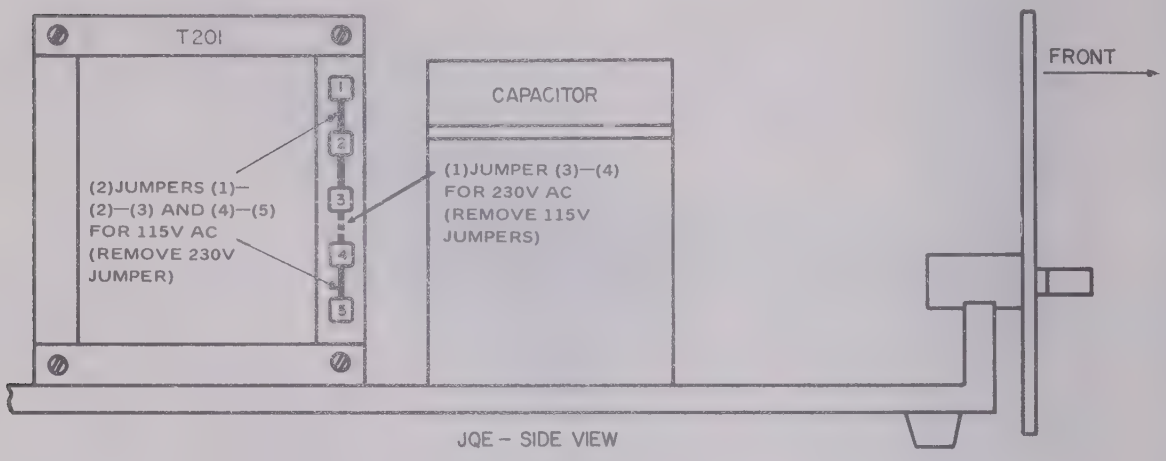


FIG. 2-2 CONVERSION FOR 230V AC NOMINAL LINE

2-6 COOLING

2-7 The power transistors and rectifiers in this power supply are maintained within their operating temperature range by means of a high effiency heat-sink assembly, cooled by an internal fan. SIDE PANEL OPENINGS AND THE TOP OF THE CASE MUST BE KEPT CLEAR FROM OBSTRUCTIONS TO INSURE PROPER AIR CIRCULATION. Periodic cleaning of the interior of the power supply is recommended. If the power supply is rack mounted, or installed into confined spaces, care must be taken that the ambient temperature does not rise above the limit specified (Refer to Section I).

2-8 PRELIMINARY CHECK—OUT

2-9 A simple operating check after unpacking and before permanent installation is advisable, to ascertain whether the power supply has suffered damage resulting from shipment. Please refer to FIG. 2-3 for the location of the operating controls and output terminals.

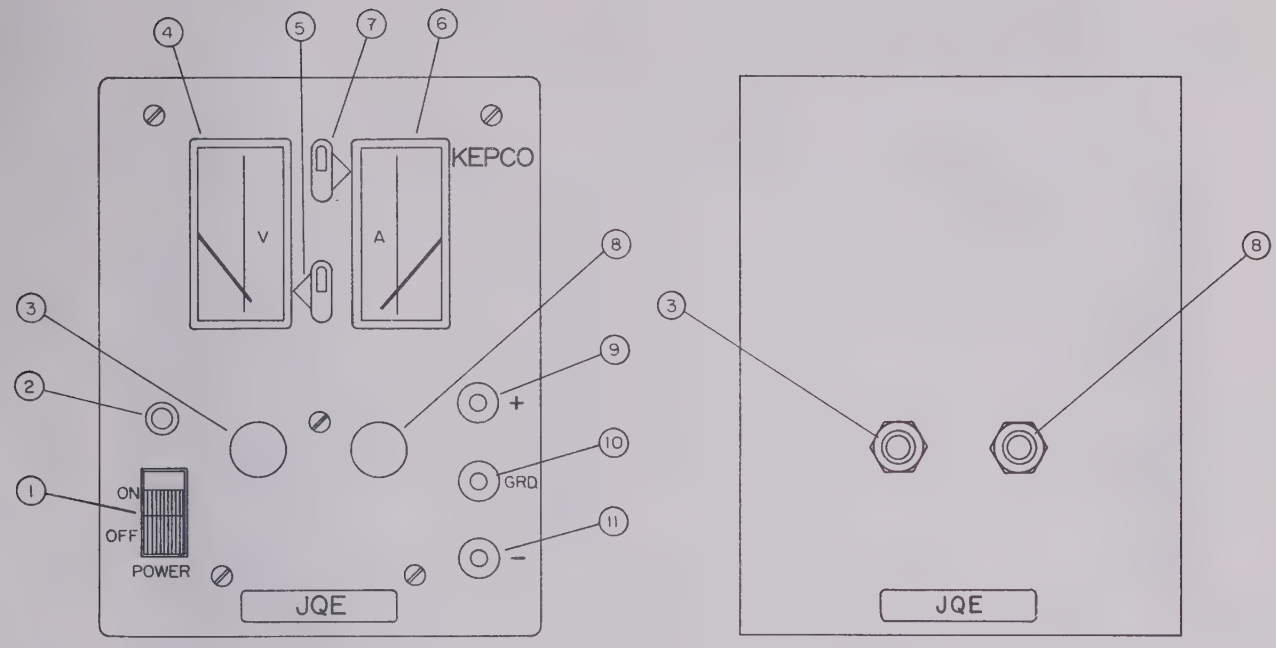
a) Connect power supply to 115V AC line or refer to paragraph 2-6 for 230V AC operation if required.

b) Turn CURRENT LIMIT CONTROL fully clockwise. Turn VOLTAGE CONTROL fully counterclockwise. Both, VOLTMETER RANGE* and AMMETER RANGE* switch should be in the "x1" position.

c) Turn AC POWER SWITCH* "on". The AC PILOT LIGHT* should be energized. Slowly turn VOLTAGE CONTROL clockwise and observe the gradual increase of the output voltage. Turn counterclockwise again until about one tenth of the maximum output voltage is reached. Set VOLTMETER RANGE* switch to the "x0.1" position. The VOLTMETER* should now read full scale again. Return both, AMMETER and VOLTMETER RANGE SWITCHES* to the "x1" position.

d) (Models suffixed "—VP" only) Check the "crowbar" action of the overvoltage protector by first "OVERVOLTAGE LEVEL" adjustments (R412,413 refer to FIG. 2-1) and turn the "Coarse" adjustment (R413) slowly counterclockwise until the output voltage goes suddenly to zero as observed on the panel meter. Turn AC POWER SWITCH* "off". Turn the "OVERVOLTAGE LEVEL" control (R413) fully clockwise again.

*On models with suffix "M" only. Connect appropriate external metering and AC input switching to meterless models.



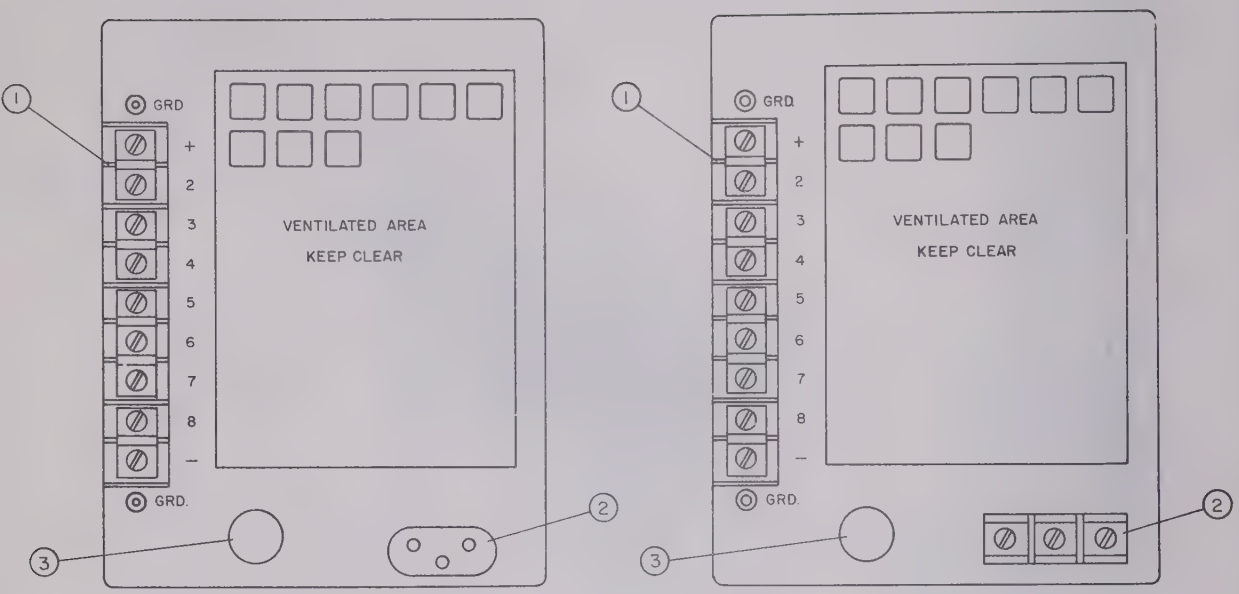
a) METERED MODELS (SUFFIX "M")

b) METERLESS MODELS

FIG. 2-3 CONTROLS AND TERMINATIONS, JQE ¼ RACK GROUP, FRONT

NO.	CONTROL OR TERMINATION	FUNCTION
1	AC POWER SWITCH	TURNS AC POWER LINE "ON" AND "OFF".
2	AC PILOT LIGHT	ENERGIZES WHEN AC POWER IS "ON".
3	VOLTAGE CONTROL	ADJUSTS OUTPUT VOLTAGE FROM ZERO TO $E_{o\ max.}$
4	VOLTMETER	MONITORS OUTPUT VOLTAGE 0–$E_{o\ max.}$ OR 0–$E_{o\ max./10.}$
5	VOLTMETER RANGE SWITCH	CHANGES METER RANGE FROM 0–$E_{o\ max.}$ TO 0–$E_{o\ max./10.}$
6	AMMETER	MONITORS OUTPUT CURRENT 0–$I_{o\ max.}$ OR 0–$I_{o\ max./10.}$
7	AMMETER RANGE SWITCH	CHANGES METER RANGE FROM 0–$I_{o\ max.}$ TO 0–$I_{o\ max./10}$
8	CURRENT LIMIT CONTROL	ADJUSTS CURRENT LIMIT FROM 0–105% $I_{o\ max.}$
9	PLUS (+) OUTPUT	POSITIVE, LOAD OR MONITORING TERMINAL
10	GROUND	GROUNDING TERMINAL
11	MINUS (–) OUTPUT	NEGATIVE, LOAD OR MONITORING TERMINAL

TABLE 2-2 CONTROLS AND TERMINATIONS, JQE ¼ RACK GROUP, FRONT



a) METERED MODELS (SUFFIX "M")

b) UNMETERED MODELS

FIG. 2-4 REAR TERMINATIONS, JQE ¼ RACK GROUP,REAR

NO.	TERMINATION	FUNCTION
1	REAR BARRIER-STRIP, WITH THESE TERMINAL FUNCTIONS:	1 POSITIVE (+) LOAD TERMINAL 2 POSITIVE (+) ERROR SENSING TERMINAL (COMMON) 3 REFERENCE VOLTAGE (E_r) (+6.2 nom.) 4 5 REFERENCE RESISTOR (Rr) 6 NULL JUNCTION 7 VOLTAGE CONTROL (R_{VC}) 8 NEGATIVE (–) ERROR SENSING 9 NEGATIVE (–) LOAD TERMINAL
2	AC INPUT	a) ACCEPTS THREE WIRE AC POWER LINE CORD (SUPPLIED) b) 3–TERMINAL BARRIER–STRIP FOR AC LINE WIRING
3	MAIN FUSE (F201)	PROTECTS MAIN TRANSFORMER AND INPUT CIRCUIT

TABLE 2-3 REAR TERMINATIONS, JQE ¼ RACK GROUP, REAR

e) Place a short circuit across the output. Turn CURRENT LIMIT CONTROL counterclockwise. Turn AC POWER SWITCH* "on". Slowly turn CURRENT LIMIT CONTROL clockwise and observe the gradual increase in output current. Turn counterclockwise until about one tenth of the maximum output current is reached. Set AMMETER RANGE* switch to the "x0.1" position. The AMMETER* should now read full scale again.

CAUTION

Do not switch to the "x0.1" ranges before the output of the power supply (voltage or current) is below 10% of the maximum rated values.

*On models with suffix "M" only. Connect appropriate external metering and AC input switch to meterless models.

2-10 INSTALLATION (Refer to FIG. 1-3 "Mechanical Outline Drawing")

2-11 The metered JQE Power Supply (Models with suffix "M") may be used as a bench-operated instrument. For these units, a retractable "bail" is provided to lift the power supply front to a convenient viewing and operating angle. If the metered JQE supply is to be rack-mounted, the bail, the two rear feet and the side handles must be removed.

2-12 The unmetered JQE Power Supply can be directly chassis-mounted into any system. Four nylon mounting stand-offs are provided at the bottom of the unit. If no additional mechanical support is provided, the unit must always be mounted so that its weight rests on the four mounting stand-offs.

2-13 For all installations into confined spaces, care must be taken that the temperature immediately surrounding the unit does not exceed the maximum specified ambient temperature (71°C).

2-14 GROUNDING

a) AC GROUND (Models with suffix "M" only). The power supply is equipped with a 3 wire safety line cord and polarized plug. The third (green) wire in the line cord is connected to the chassis and the case of the unit. If a two terminal receptacle in combination with an adaptor is used, it is imperative that the chassis of the power supply be returned to AC ground with a separate lead.

b) AC GROUND (Unmetered Models). The power supply is equipped with a three terminal barrier-strip for AC input. The terminal marked "GRD" is directly connected to the power supply chassis and must be returned to AC ground.

c) DC GROUND. The DC output is isolated from the AC power line and from any direct connection to chassis or ground. The maximum output voltage that can be supported between either output terminal and ground or chassis is 500V DC plus the maximum output voltage of the power supply. Either side of the output may be grounded. Convenient grounding terminals are provided at the front panel binding post (on models with suffix "M") and at the rear barrier-strip.

SECTION III – OPERATION

3-1 STANDARD POWER SUPPLY OPERATION, LOCAL CONTROL

3-2 GENERAL. The JQE Power Supply is shipped from the factory with five (5) removable jumper links in place at the rear barrier strip (TB1) as shown in FIG. 3-1. THESE LINKS MUST BE IN PLACE AND SECURED THIGHTLY for standard local operation. Loose terminal links or wires at the barrier strip will cause malfunction of the power supply.

3-3 LOAD CONNECTION. The load may be connected in any of the four ways illustrated in FIG.'s 3-2/3-3. Load connecting wires should be as heavy as practicable, since load wire voltage drops will degrade regulation performance. Twisting of load wires will help to preserve the low output impedance of the JQE power supply.

FIG. 3-1
STANDARD JUMPER-LINK CONNECTIONS
JQE GROUP

3-4 REMOTE ERROR SENSING

3-5 Specified regulation performance directly at the load requires the use of remote error sensing. A twisted, shielded pair of wires from the sensing terminals directly to the load will compensate for load wire voltage drops up to 0.5 volt per wire (Refer to FIG. 3-3). Observe polarities: The negative sensing wire [from terminal (8)] must go to the negative load wire, and the positive sensing wire [from terminal (2)] goes to the positive load wire.

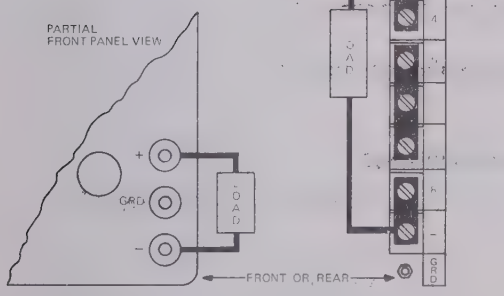


FIG. 3-2 LOAD CONNECTION WITHOUT ERROR SENSING

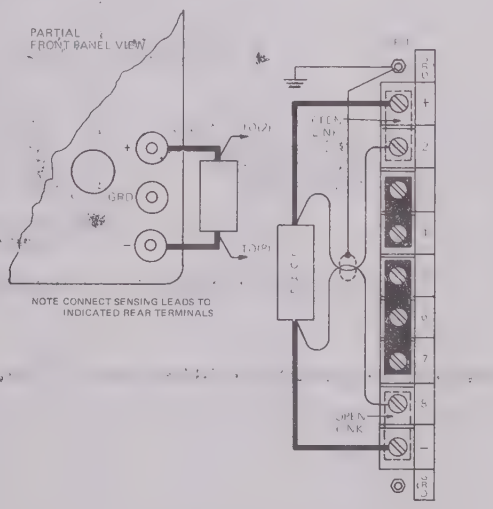


FIG. 3-3 LOAD CONNECTION USING REMOTE ERROR SENSING

NOTE: 1) If Error Sensing is not used, Specified Performance must be measured at terminals (2) and (8) of TB1.

2) Front panel load connections applicable for models with suffix "M" only.

3-6 OUTPUT ADJUSTMENT*

3-7 Once the load is connected to the output terminals of the power supply as described in the previous paragraphs, the operating voltage may be adjusted to the desired value by turning the VOLTAGE CONTROL and observing the panel VOLTMETER.** The CURRENT LIMIT CONTROL may be set fully clockwise, and will in this position provide over current protection at 105% of the maximum rated output current. The CURRENT LIMIT CONTROL may be set to intermediate values, as desired, by first turning the AC POWER SWITCH** to the "off" position and then placing a short circuit across the output. Now the required operating current is adjusted by turning the CURRENT LIMIT CONTROL to the value needed plus ≈2%. The output current will now limit sharply at this value. After the short is removed from the output, the load is reconnected and the power supply is ready for operation.

3-8 INTRODUCTION TO REMOTE PROGRAMMING

3-9 GENERAL. A few general remarks may be in order to familiarize the user of this equipment with the terminology and basic equations pertaining to remote programming of the Kepco JQE Power Supplies. Electrically, the power supply consists of the unregulated DC source (E_u), the pass element (E_p), the DC error amplifier (A) and a comparison circuit which resembles a four-arm electrical bridge. (Refer to FIG. 3-4). The elements of the bridge are arranged to produce a virtual zero at the amplifier input when the bridge circuit is at balance ($V_{AA'} = 0$). Any tendency for the output voltage to change in relation to the reference voltage (E_r) creates an error signal (ϵ) which, by means of negative feedback and the amplifier, tends to correct the output voltage towards restoration of bridge balance.

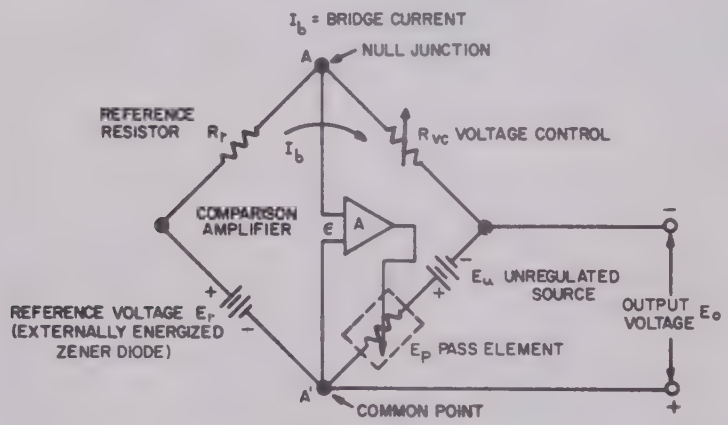


FIG. 3-4 KEPCO POWER SUPPLY AND COMPARISON BRIDGE CIRCUIT

3-10 EQUATIONS SHOWING THE OPERATION OF THE KEPCO BRIDGE

3-11 The following relationships govern the operation of the Kepco Bridge at balance, i.e., with $V_{AA'} = 0$:

a) $$\frac{E_o}{E_r} = \frac{R_{VC}}{R_r}$$

$$\frac{E_r}{R_r} = I_b \quad (2)$$

$$E_o = I_b R_{VC} \quad (3)$$

Where: E_o = Output Voltage
E_r = Reference Voltage
R_r = Reference Resistance
R_{VC} = Control Resistance
I_b = Bridge Current

b) The ratio of the number of ohms control-resistance needed per volt output is termed the "CONTROL RATIO". It is nominally 1000 ohms per volt in the JQE Power Supplies.

*NOTE: (Models with suffix "−VP" only) Refer to FIG. 6-3 for the operating instructions on the OVERVOLTAGE PROTECTOR.

**On models with suffix "M" only. Connect appropriate external metering and AC input switching to meterless models.

c) As can be seen from equation (1), the output voltage E_o can be controlled by varying any one of the three quantities. Rewriting equation (1) we have:

$$E_o = \frac{E_r}{R_r} \times R_{vc}$$

The ratio $\frac{E_r}{R_r}$ constitutes the bridge current I_b. (Eq.2)

d) Therefore, we can write: $E_o = I_b R_{vc}$ (Eq.3).
Making I_b a precision quantity (precision bridge current adjustment is described in par. 3-16), establishes a precise programming ratio, so that the accuracy of E_o is solely dependent upon R_{vc}. This mode of operation is referred to as "RESISTANCE PROGRAMMING" and is covered in detail in Par, 3-19.

e) Rewriting Equation (1), $E_o = E_r \frac{R_{vc}}{R_r}$, we can make E_r the variable which controls E_o.

This type of control is referred to as "VOLTAGE PROGRAMMING" and is covered in Par. 3-5.

f) Many other modes of control are of course possible; some of them are described in the following paragraphs. For a more extensive treatment and a detailed theoretical view of power supply applications, see the "Kepco Power Supply Handbook", available from your Kepco Representative or directly from Kepco Applications Engineering Department.

NOTE: For all programming and adjustment components, use high quality, wire-wound, resistors with a T.C. of 20 p.p.m. or better.

3-12 ADJUSTMENTS FOR EXACT PROGRAMMING RATIO

3-13 Referring to equation (3): $E_o = I_b R_{vc}$, it is seen that if $I_b = 1$ mA, 1000 ohms of control resistance (R_{vc}) is needed for each volt of output. Once I_b is therefore adjusted precisely, the accuracy and linearity of the output voltage will then solely depend upon R_{vc}.

3-14 Again refering to equation (3), we see that if $R_{vc} = 0$ (shorted out), the output voltage should be zero. A small negative offset voltage (in the millivolt range) exists however under this condition at the output.

3-15 Both inaccuracies, the slightly larger bridge current (I_b) and the small negative offset voltage may be adjusted to provide a linear and precise programming ratio. While the "I_b Adj." control (R4) is a standard feature in all models, the "E_{io} null" (R14) or offset zeroing control is provided only in models bearing the suffix "E" (Model JQE 6—10ME for example). This control can however be added by the user (see paragraph 5-8).

3-16 PROCEDURE, PRECISION PROGRAMMING RATIO ADJUSTMENT (Refer to FIG. 3-5)

a) Equipment Required:

1) Precision digital or differential voltmeter (M1).
2) Precision resistor, accuracy comparable to M1. The value is not important, but must be known. For every 1000 ohms 1 volt will appear across M1. (R_{vc}).
3) Single pole, single throw switch (S1).

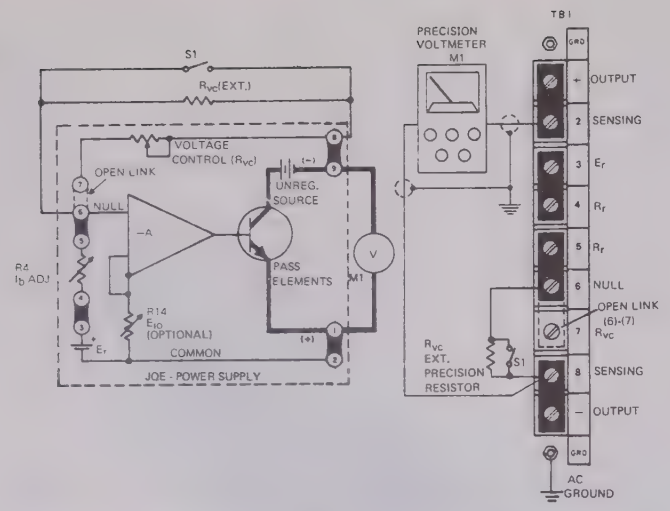


FIG. 3-5 CONNECTIONS FOR PRECISION PROGRAMMING RATIO ADJUSTMENT

b) Connect calibration set-up as shown in FIG. 3-5 and connect the JQE power supply to the AC power line.

c) With S1 "open", and R_{VC} = 5 K ohms, approximately 5 volts will be read-out on M1. Adjust "I_b Adj." (bridge current adjustment, R4, see FIG. 2-1 for location) until exactly 5 volts are read-out on M1.

d) Close S1 and note deviation from zero on M1 (approx. 2 to 8 mV negative). Adjust "E_{io} null" (zero adjustment, R14, see FIG. 2-1 for location) until exactly zero volts are read-out on M1.

e) Open S1 and check the 5 volt reading. Repeat "C" and "D", as necessary to achieve the desired accuracy. FIG. 3-6 below shows graphically how the programming ratio can be precision adjusted.

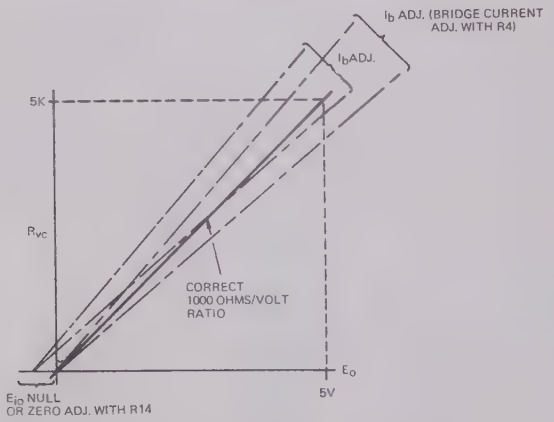


FIG. 3-6 ESTABLISHING PROGRAMMING LINEARITY

3-17 GENERAL RULES FOR REMOTE PROGRAMMING

3-18 All remote programming applications require the observance of a few basic rules to insure proper functioning of the power supply in the particular mode of operation selected. These rules should be remembered before each application and especially if malfunctioning of the programming set-up is experienced.

1) GROUNDING. Ground loops due to multiple and indiscriminat grounding of various equipment are the cause for the majority of complaints about "high ripple or noise". They can be easily avoided by grounding only the power supply at one point, to which all shielded cables and equipment cases are connected. If possible, one side (preferably the positive one) of the output of the power supply should also be grounded. Frequently, high ripple is introduced by programming sources or instruments used at the input (null junction) although all grounding rules have been observed. This may be due to the external instruments or source's close coupling to the AC line. In these cases only isolation (i.e. battery

operated devices) is the answer. It must also be remembered that input and output of the power supply are "common", so that if the output is not grounded, the input source must be isolated for the maximum output voltage to be programmed, plus the amount of voltage the common is "off ground".

2) CONNECTIONS. All external connections, especially the rear barrier strip terminal screws, must be tight. Serious malfunctions may be caused by open feedback loops or other open programming connections.

3) INPUT SOURCES. If specified performance is expected when remote programming, external input or reference sources must have specifications equal to or better than that of the power supply.

4) PROGRAMMING RESISTORS. Programming resistors should be high quality wirewound units with temperature coefficients of 20 parts per million or better. Their wattage rating must be at least 10 times the actual power dissipated. Although the control current through these resistors is only 1 mA, an error current exists when programming large voltage excursions. The magnitude of this error current equals the change in output voltage, divided by the final resistance of the programming resistor. (If, for example, the voltage step is from 50 volts to zero ($\Delta E_o = 50$ V), and the final resistance of the programming resistor is $\Delta R_{VC} = 2$ ohms, $I_{peak} = 25$ A.) The duration of the peak error current depends upon the size of the output capacitor. I_{peak} decays exponentially as the output voltage assumes the final value.

If step-switch devices are used in resistance programming, they must be of the "make before break" variety to avoid programming infinity. Programming resistors must have a voltage rating at least equal to the maximum output voltage of the power supply.

3-19 OUTPUT VOLTAGE PROGRAMMING WITH EXTERNAL RESISTANCE

The output voltage of the JQE Power Supply may be controlled remotely by an external resistance, replacing the built-in voltage control resistance which is disconnected at the rear barrier strip. The value of the programming resistance may be calculated by referring to the transfer function derived earlier (Eq.1):

$$E_o = R_{VC}\left(\frac{E_r}{R_r}\right)$$

Since $\frac{E_r}{R_r} = I_b$ (2), it follows that $E_o = I_b R_{VC}$ (3).

Referring to Equation (3), we see that since I_b is 1 mA in the JQE supplies (and can be precisely adjusted as shown in Par. 3-12), for every volt of output, 1000 ohms control resistance must be provided. This corresponds to a "Programming Ratio" of 1000 ohms per volt.

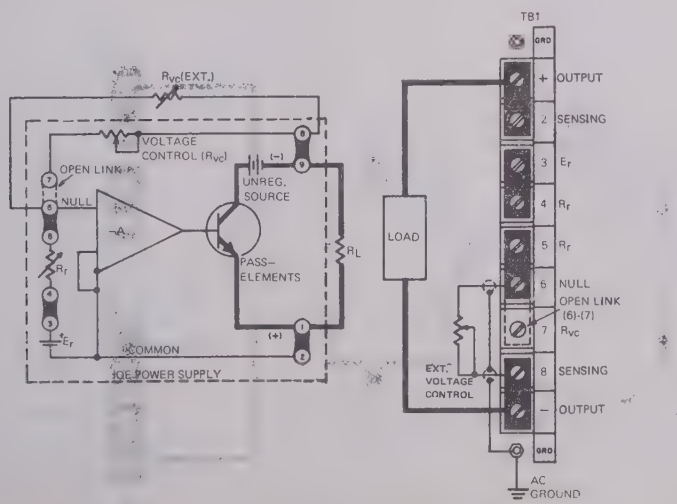


FIG. 3-7 REMOTE RESISTANCE PROGRAMMING

3-20 PROCEDURE (Refer to FIG. 3-7)

a) Determine value of programming resistor(s) for output voltage desired.

b) Using two-wire, shielded cable, connect the chosen resistors to terminals (6) and (8). Connect the shield to the "ground" terminal.

c) The output voltage will vary from zero to (1 mA) x (R_{VC}), as R_{VC} is adjusted from zero to its maximum value.

3-21 PROGRAMMING BY CONDUCTANCE

3-22 For special applications, the output voltage E_O can be adjusted by varying the bridge current. The relationship governing this type of programming is:

$$E_O = E_r (R_{VC}) (G), \text{ (Eq. 4)} \qquad \text{Where: } G = \frac{1}{R_r + R_x} = \text{programming conductance.}$$

$$\text{or } E_O = \frac{E_r}{R_r + R_x} \times (R_{VC})$$

E_O = output voltage
E_r = reference voltage
R_{VC} = control resistance
R_r = reference resistance
R_x = programming resistor

Since $\frac{E_r}{R_r} = I_b$ and $E_O = I_b R_{VC}$ the output voltage varies directly as I_b changes. Changing I_b with the help of an additional resistor in series with R_r results in an inversely proportional change of I_b, since now

$$I_b = \frac{E_r}{R_r + R_x} .$$

This method of output voltage adjustment is therefore referred to as conductance programming.

3-23 Conductance programming is a reciprocal function when analyzed in terms of resistance, but perfectly proportional

$$\left(E_O \propto \frac{1}{R_x} \right).$$

It can be very useful, especially over a limited range and for small changes in output voltage. Another distinctive advantage of this type of programming is the "built-in" safety feature. Should the programming circuit open accidently, the programming resistance becomes infinite, the conductance is zero, and consequently, the voltage becomes zero.

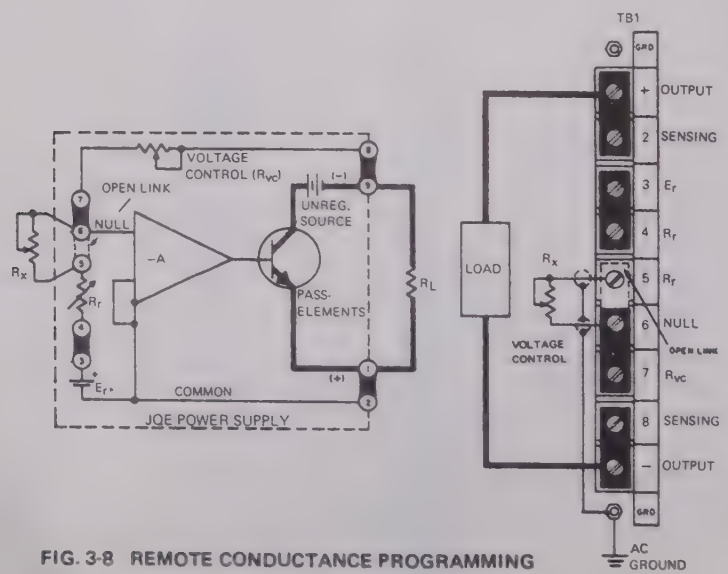


FIG. 3-8 REMOTE CONDUCTANCE PROGRAMMING

3-24 PROCEDURE (Refer to FIG. 3-8)

EXAMPLE: E_O desired = 3 to 5 volts.

a) Select R_{VC} for maximum desired output voltage E_O.

$$R_{VC} = \frac{E_O}{I_b}, \quad R_{VC} = \frac{5\,V}{1\,mA} = 5\ K\ ohms.$$

b) A change of E_O to 3 volts requires a bridge current change of:

$$\Delta I_b = \frac{\Delta E_O}{R_{VC}} = \frac{2\ volts}{5\ K\ ohms} = .4\ mA$$

c) Since $I_b = \frac{E_r}{R_r} = 1\ mA$; the additional resistance needed is:

$$I_b (R_r + R_x) = E_r, \ R_x = \frac{E_r}{I_b} - R_r \text{ or } R_x = \frac{6\ volts}{0.6\ mA} - 6\ K = 4\ K\ ohms.$$

A 4K ohm, WW, low T.C. potentiometer will change the output voltage from 3 to 5 volts when varied from zero to 4K ohms.

NOTE: A word of caution may be in order in regard to the changing of the bridge current. The 1 mA value has been selected for maximum stability in the zener reference circuit. A large departure from this value is not advisable. If a ±50% change in I_b is not sufficient to achieve the desired voltage swing, an external reference supply can be used.

3-25 REMOTE PROGRAMMING WITH EXTERNAL CONTROL VOLTAGE

3-26 The output voltage of the power supplies in the JQE group may be controlled by an external, variable voltage source, which supplies the necessary control current, formerly delivered by the (now disconnected) internal, fixed reference source. This mode of operation is termed "Voltage Programming". It can be mathematically expressed by referring to basic equation (1):

$$\frac{E_O}{E_r} = \frac{R_{VC}}{R_r} \qquad \text{(Eq. 1)}$$

Solving for E_O, and designating the external programming components "E_i, R_i", the result is:

$$E_O = -E_i \frac{R_{VC}}{R_i} \qquad \text{(Eq. 5)}$$

3-27 As seen from the expression (Eq.5), if the ratio R_{VC}/R_i (which represents the closed loop gain of the system) is held constant, E_O will vary linearly with E_i, the external programming voltage. The above expression (Eq.5) is seen to be similar to the operational amplifier transfer function in the inverting configuration. The voltage programmed power supply does in fact become a unipolar power amplifier with potentially very high power gain but with limited frequency response. (Refer to FIG. 3-9). The limited frequency response is largely due to the effect of the output capacitor (C_O) acting as a low-pass filter and preventing the output voltage (E_O) from varying rapidly. The output voltage may be varied over the full range, bounded on the high end by the maximum rated value. A practical example to illustrate component selection will be given below.

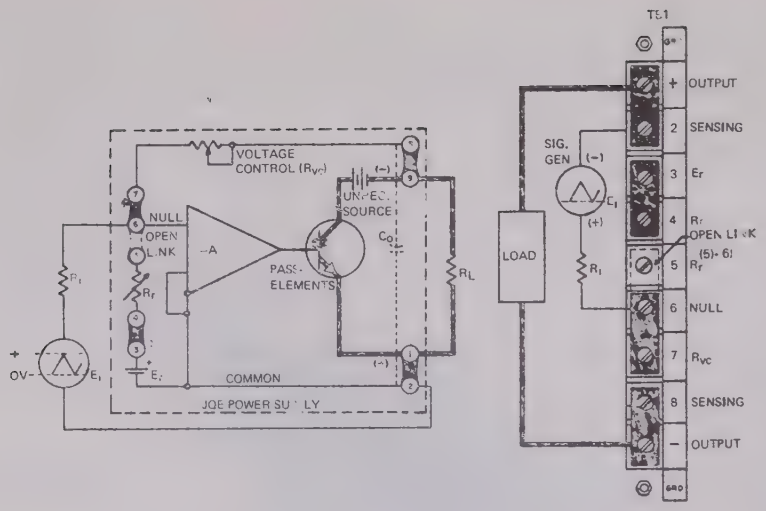


FIG. 3-9 JQE IN THE OPERATIONAL AMPLIFIER MODE

3-28 Example: A Kepco Model JQE 36–3M is to be voltage programmed over its full range (0 to 36V) by a triangular, positive increasing source with a peak output voltage (E_i) of 2 volts, and able to deliver at least 1 mA control current. Since the required voltage gain is: $E_O/E_i = 36/2 = 18$, the ratio R_{VC}/R_i must also equal 18. If the internal voltage control is retained, in this case, since $R_{VC} = 40$ K ohms, R_i must equal 40 K/18 = 2.22 K ohms. FIG. 3-9 shows the JQE Power Supply as an operational amplifier and the necessary rear barrier connections for this mode of operation.

3-29 CONSTANT CURRENT OPERATION

3-30 The JQE power supply can be set up to deliver constant current by the addition of an external sensing resistor and a current control. In the constant current operating mode, the voltage comparison bridge is interconnected with an external current sensing resistor, R_S, and a current control, R_{CC}, (as shown in FIG. 3-10) to maintain a constant voltage drop across R_S. In this way, an adjustable constant load current is obtained. Characteristic of the constant current supply is ability to change its output voltage automatically in order to maintain a constant current through a range of possible load resistances. The range of output voltage that the supply can deliver and simultaneously maintain constant current, is referred to as the "COMPLIANCE VOLTAGE".

3-31 The current sensing resistor R_S is chosen to develop 1 volt drop at the maximum desired current. It is calculated by dividing this current into one volt. The value of R_S is not critical and can be the nearest standard resistance available. Several facts should be kept in mind however, when choosing R_S. A compromise must be made between a large and a small value. While a large value is desirable for good current regulation, it is less so in view of the power dissipated across it. It must be remembered that all the load current is flowing through R_S and the input to the regulator is connected across it. It is therefore vital that all extraneous changes across R_S, i.e. resistance change due to temperature, are kept to a minimum. A high quality, low T.C. (20 PPM) resistor, at least ten times the actual wattage needed, is therefore strongly recommended. In practice, R_S is usually selected for a 1 volt sample at maximum output current desired. If more than a 10:1 ratio of output current is needed several values of R_S should be selected and switched in or out as required.

3-32 The current control resistor, R_{CC}, is chosen on the basis of the control ratio of the Kepco comparison bridge, and V_S, the maximum voltage across R_S. If R_S was selected for 1 volt drop, then $V_S = 1$ volt and $R_{CC} = V_S$ times (control ratio), or

$$R_{CC} = 1 \text{ volt} \times \frac{1000 \text{ ohm}}{1 \text{ volt}} = 1000 \text{ ohms.}$$

A high quality, low T.C. (20 PPM) resistor is recommended for R_{CC}.

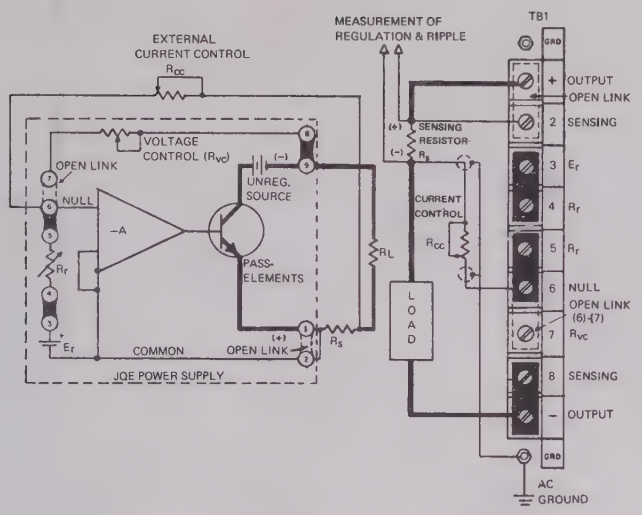


FIG. 3-10 CONSTANT CURRENT OPERATING MODE

3-33 PROCEDURE (Refer to FIG. 3-10)

The actual component calculation for current regulation is perhaps best shown by a practical example. A JQE 100–1M is to be set up for current regulated output from 30 mA to 300 mA. (1:10 ratio).

a) R_S is chosen by: $R_S = \frac{1 \text{ volt}}{0.3 \text{ A}} = 3.33 \text{ ohm.}$

$P_{diss} = V_S \times I_{max.} = 1 \text{ volt} \times (0.3 \text{ A}) = 0.3 \text{ watts.}$

Three 10 ohm, 1 W, wire-wound resistors connected in parallel will be satisfactory.

b) The control resistance R_{CC} is found by calculating its limits:

$$R_{CC}\,(\text{max.}) = V_S\,(\text{max.}) \times \text{control ratio} = 1 \text{ V} \times \frac{1000 \text{ ohms}}{\text{V}} = 1000 \text{ ohms.}$$

$$R_{CC}\,(\text{min.}) = V_S\,(\text{min.}) \times \text{control ratio} = 0.1 \text{ V} \times \frac{1000 \text{ ohms}}{\text{V}} = 100 \text{ ohms.}$$

As R_{CC} is therefore varied from 100 ohms to 1000 ohms, the regulated current will vary from 30 to 300 mA. The compliance voltage will change in the same ratio (10:1), its absolute value depending on the load resistance used.

c) The full output current range (1 mA to 1 A) could be covered if three separate sensing resistors were to be calculated and switched into the sensing circuit. Each would cover a range of 10:1, and all would be calculated on the basis of a 1 volt sample at maximum current:

a) R^1_S (for 0.1 to 1 A) = 1 V/1 A = 1 Ω (10 watt).

b) R^2_S (for 0.01 A to 0.1 A) = 1 V/0.1 A = 10 Ω (1 watt).

c) R^3_S (for 1 mA to 0.01 A) = 1 V/0.01 A = 100 Ω (1 watt).

3-34 SERIES OPERATION

3-35 General. Kepco JQE Power Supplies can be series-connected for increased voltage output, provided the specified limits on voltage to chassis are not exceeded. When series-connected, the supplies should be protected by means of a semi-conductor diode across the output terminals of each power supply, as shown in FIG. 3-11. The peak inverse rating of these diodes must be at least as large as the output voltage of the supply to which they are connected. The continuous current rating of the diodes should be at least as great as the largest short-circuit current of the interconnected supplies.

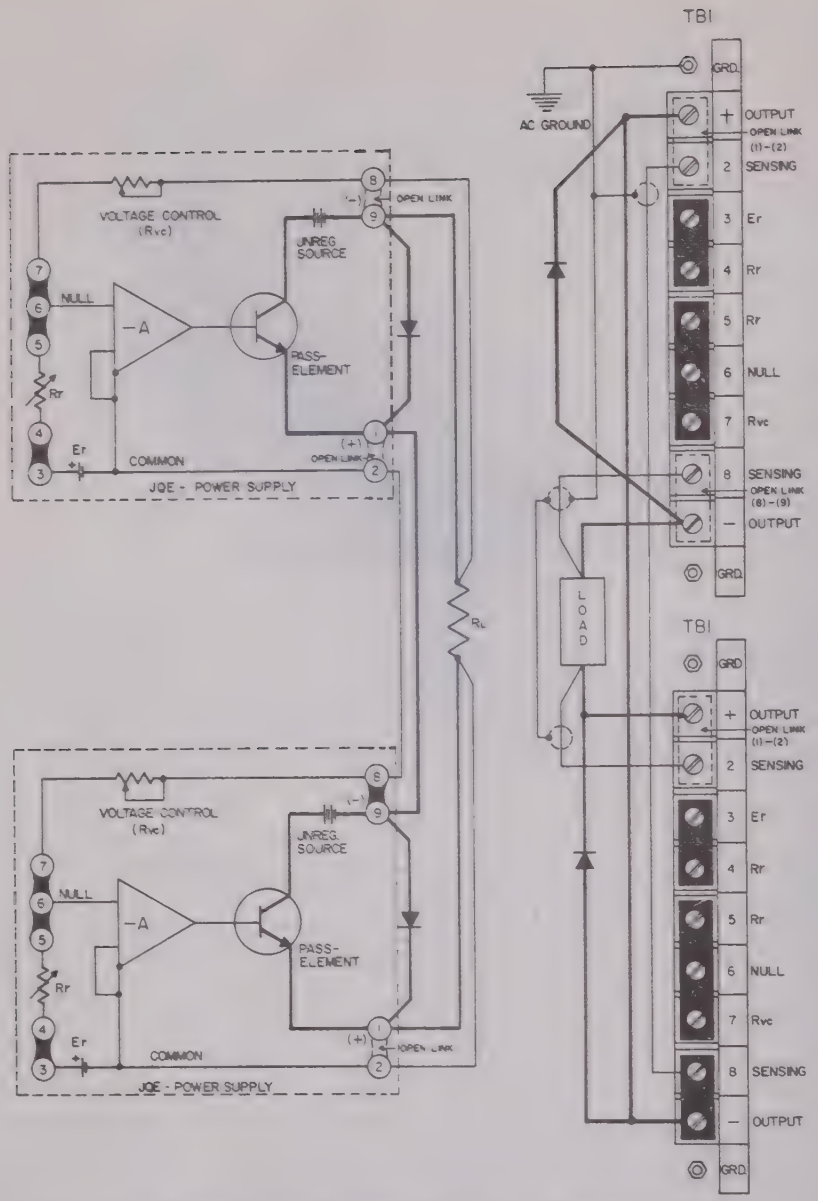


FIG. 3-11 AUTOMATIC SERIES CONNECTION OF JQE POWER SUPPLIES

3-36 PROCEDURE

a) Connect load as shown in FIG. 3-11. Keep voltage drop in load wires as low as practical by using heavy gauge wire.

b) Connect protective diodes across respective output terminals.

c) Remove jumpers as shown and connect error sensing leads. These leads carry negligible current and can be approximately #18 gauge wire.

d) Turn supplies on and adjust voltage on either control as required.

3-37 An alternate method of series connecting two or more power supplies is shown in FIG. 3-12. The basic difference between the "Automatic" series connection, shown in FIG. 3-11, and the "Master–Slave" connection (FIG. 3-12) is that with the former connection the outputs of both supplies may be controlled or programmed individually, while with the latter method only the "Master" supply is controlled while the "Slave" supplies follow the command of the "Master" in a ratio which may be pre-determined by the user. The "Master–Slave" series connection is therefore often termed an "Automatic Tracking" configuration.

3-38 The principle of operation of the "Master–Slave" series connection is as follows:
As seen from FIG. 3-12, the reference voltage of the "Slave" supply is disconnected and its input (null junction) is connected to the output of the "Master" supply. Since the control current for the "Slave" supply is thereby derived from the "Master", the "Slave" output is thus completely dependent on the "Master" supply output.

$$E_{os} = E_{om} \frac{R_{vcs}}{R_t} \quad ;$$

Where: E_{om} = Output Voltage, Master
E_{os} = Output Voltage, Slave
R_t = Tracking Resistor
R_{vcs} = Voltage Control Resistor, Slave.

As seen from the equation, if the tracking resistor (R_t) is made equal in value to the voltage control resistor of the "Slave" (R_{vcs}), a tracking ratio of 1:1 is achieved, and the output of the "Slave" will equal that of the "Master". If a single load is connected to the series combination (FIG. 3-12), twice the "Master" output is applied to it, if separate loads are connected, identical voltages are applied to the individual loads. The ratio E_{os}/E_{om} can be readily changed if the application so requires by simply changing either R_{vcs} or R_t.

3-39 PROCEDURE

a) Connect load(s) as shown in FIG. 3-12.
b) Remove jumpers as shown and connect error sensing leads if required.
c) Turn supplies "on" and adjust output on the "Master" supply as required.

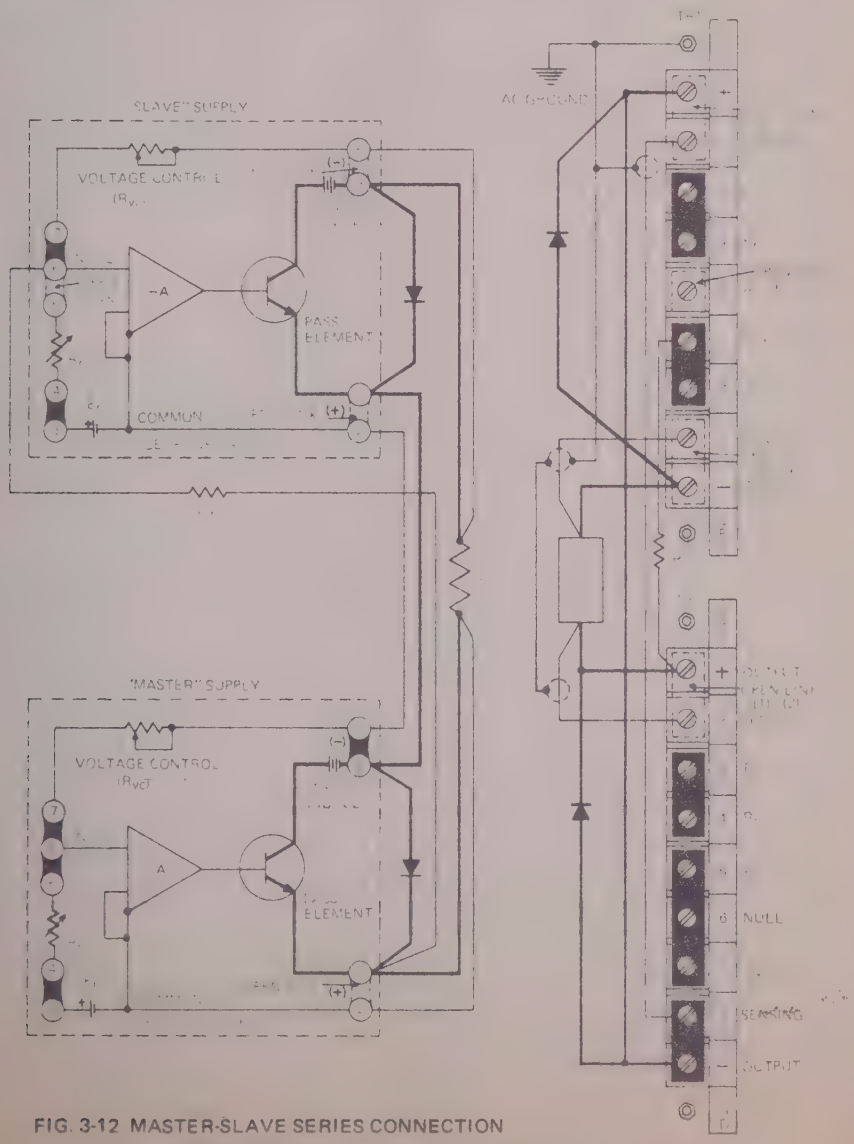


FIG. 3-12 MASTER-SLAVE SERIES CONNECTION OF JQE POWER SUPPLIES

3-40 PARALLEL OPERATION

3-41 [illegible] BHK Power Supplies can be parallel connected for increased load current output. As in the [illegible] described series connections, an "automatic" or a "master-slave" connection method can be [illegible]. The basic difference between the two suggested methods are the manner in which the output is controlled. The "Automatic" method requires individual output control from each power supply, while with "Master-Slave" method single control is exercised from the "Master" supply. For either method some general rules apply which should be observed in paralleling power supplies:

1. Connect only supplies with identical compliance voltage range.
2. Error sensing (from either supply) may be used as shown in the diagrams. Close links if this is not desired.
3. Load wires should be as short as practicable. Select the wire gage as heavy as possible and twist tightly. Approximately equal lengths of wire should be used.
4. Common AC power turn-on for all supplies is recommended.

3-42 AUTOMATIC PARALLEL CONNECTION. Each supply is set approximately to the desired output voltage with its respective Current Limiting Adjustment at factory adjusted value (105% I_O max.). After paralleling the two power supplies, one of the (supply #1) will inherently be at a slightly higher output voltage than the other (supply #2). Consequently, supply #1 will deliver all the load current up to the setting of its Current Limiting Adjustment. As the load is increased beyond this limit of supply #1, supply #2 takes over and delivers the additional current. The Current Limiting Adjustment of supply #1 can now be decreased, so that approximately equal current sharing is obtained. FIG. 3-13 shows in form of a diagram, how the two supplies operate in parallel, with their respective Current Adjustments at the factory adjusted value (105% I_O max.). It will be obvious from the diagram, that the areas of load regulation are within the output current bands of the individual supplies. Therefore, load regulation cannot be measured from zero to twice the load current for example, but only within the individual load current bands. Error sensing as described in Par. 3-4, (from either supply) may be used if precise regulation at the load is required.

CAUTION

When using the "AUTO–PARALLEL" method, care should be excercised to avoid turning the voltage control of only one supply close to zero. This precaution is necessary to prevent possibly damaging currents in the voltage control resistor as its limiting resistance is lowered.

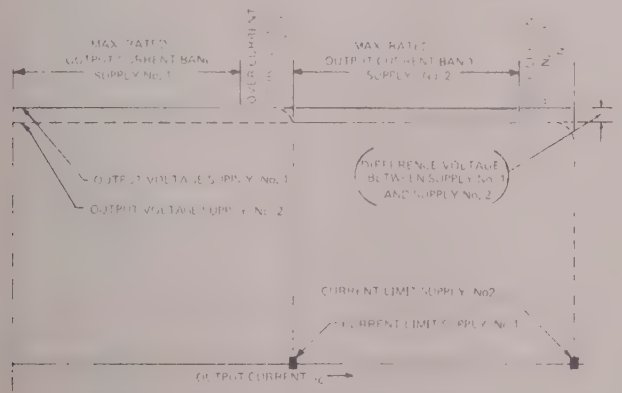

MAX. RATED OUTPUT CURRENT BAND SUPPLY No. 1
MAX. RATED OUTPUT CURRENT BAND SUPPLY No. 2
OUTPUT VOLTAGE SUPPLY No. 1
OUTPUT VOLTAGE SUPPLY No. 2
DIFFERENCE VOLTAGE BETWEEN SUPPLY No. 1 AND SUPPLY No. 2
CURRENT LIMIT SUPPLY No2
CURRENT LIMIT SUPPLY No 1
OUTPUT CURRENT

FIG. 3-13 PARALLEL OPERATION, JQE SUPPLIES

3-43 [illegible] JQE (Refer to FIG. 3-14)

1. [illegible] units as shown in FIG. 3-14. Open SW-1 and connect to line.
2. [illegible] units to the approximate output voltage desired.
3. Close SW-1. Observe load current meters M1 and M2. Adjust Current Limit Control on the unit showing the higher current on its load current meter. Turn Current Limit Control counter clockwise until current on M-1 and M-2 are approximately equal.

3-44 MASTER–SLAVE PARALLEL CONNECTION. Two JQE power supplies are shown in FIG. 3-15, although more can be paralleled, each unit requiring its own sensing resistor as shown. The sensing resistors (R_{S1}, R_{S2}) are of equal value and are selected such that the voltage drop across them is either 0.5 volt (for models with output current ratings of 5 amperes and above) or 1 volt (for models below 5 A) at the maximum desired operating current. Selection of these resistors must be done with care. Please refer to paragraph 3-31 which should illustrate the process. High current power supplies when connected in parallel in the "Master–Slave" configuration require sometimes rather non-conventional values: for two 10 ampere supplies, the sensing resistors should be: $R_{S1} = R_{S2} = 0.5V/10 = 0.05$ ohm, Wattage: $0.5 \times 5 \times 10 = 25$ watt. Heavy duty ammeter shunt resistors, or special heavy duty units from the TEPRO "TMK" series (TEPRO ELECTRIC CORP., Rochester, New York) are idealy suited for sensing resistors. Load connections from all supplies (Heavy lines in FIG. 3-14) should be of equal lengths, as heavy a wire gauge as practicable and as short as possible. Error sensing leads from the "Master" supply should be twisted and shielded as indicated.

3-45 PROCEDURE FOR "MASTER–SLAVE" PARALLEL CONNECTIONS

a) Select external current sensing resistors (R_{S1}, R_{S2}) as described.

b) Connect supplies as shown in FIG. 3-14, keeping load and error sensing leads as short as possible. Use shielded wire for the connection from terminal (2) of the "Master" to terminal (6) of the "Slave" unit.

c) Connect supplies to common AC power line and use common power switching.

d) After turn-on, output voltage can be adjusted on the Voltage Control of the "Master" supply and operation can commence.

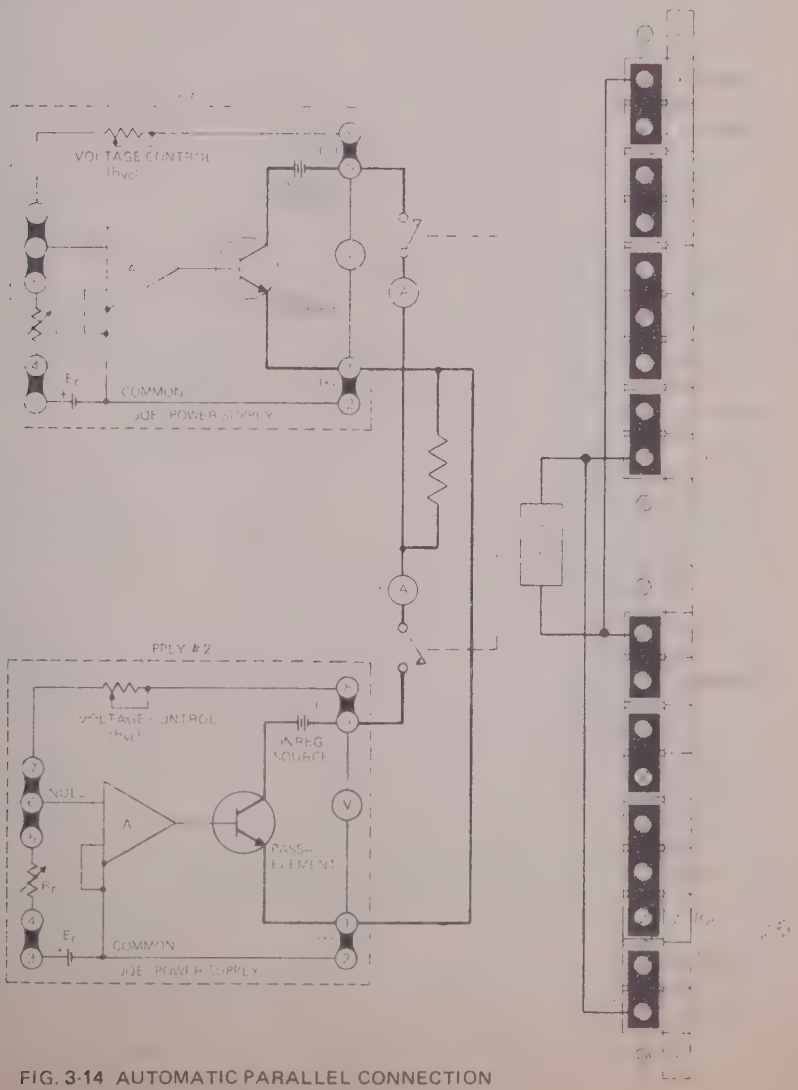


FIG. 3-14 AUTOMATIC PARALLEL CONNECTION OF JQE POWER SUPPLIES

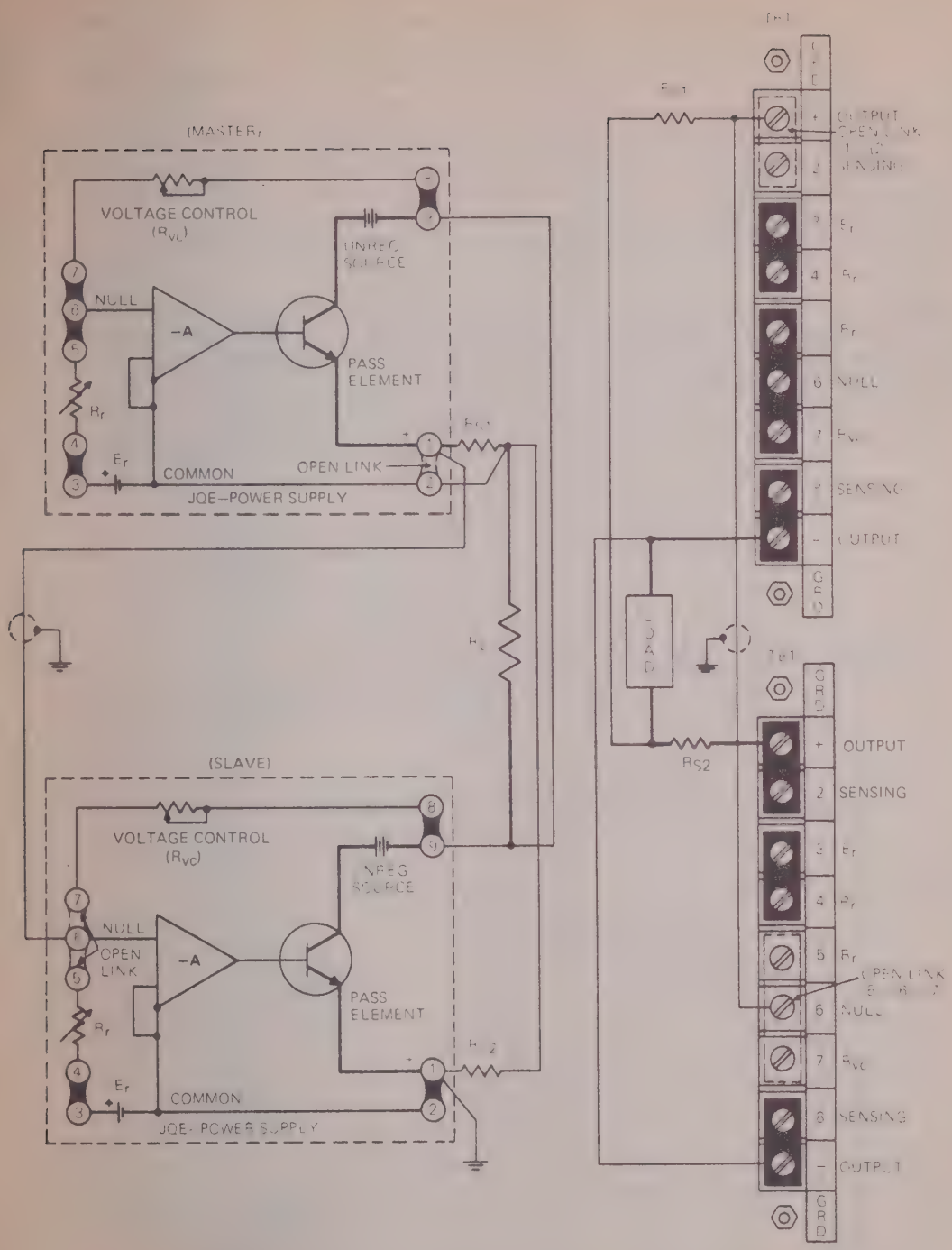


FIG. 3-15 MASTER-SLAVE PARALLEL CONNECTION OF JQE POWER SUPPLIES

SECTION IV – THEORY OF OPERATION

(References in CAPITAL LETTERS refer to the nomenclature used in FIG. 4-1)

4-1 SIMPLIFIED DIAGRAM DISCUSSION (Refer to FIG. 4-1)

4-2 The MAIN POWER TRANSFORMER converts the line voltage to the required levels needed to produce the DC operating voltages for the MAIN AND AUXILIARY SUPPLIES. The MAIN POWER SUPPLY is in series with the PASS ELEMENTS and the CURRENT SENSING RESISTOR and produces the output voltage. The MAIN PASS ELEMENTS are changing their series resistance in a way tending to keep the OUTPUT VOLTAGE constant. The necessary drive for this change is produced by the DRIVER CIRCUIT which in turn receives its command signals either from the CURRENT LIMIT AMPLIFIER or the ERROR SIGNAL AMPLIFIER.

4-3 The OUTPUT VOLTAGE is constantly compared to the REFERENCE SOURCE, while the output current is monitored by the CURRENT SENSING resistor. Any change, in either output voltage or current, is amplified by the assoicated amplifier and transfered to the GATE DIODES which feed directly into the DRIVER CIRCUIT and produce the needed drive signal for the MAIN PASS ELEMENTS, thus either keeping the OUTPUT VOLTAGE constant, or if the signal from the CURRENT LIMIT AMPLIFIER is dominant, limiting the output current to the pre-adjusted value.

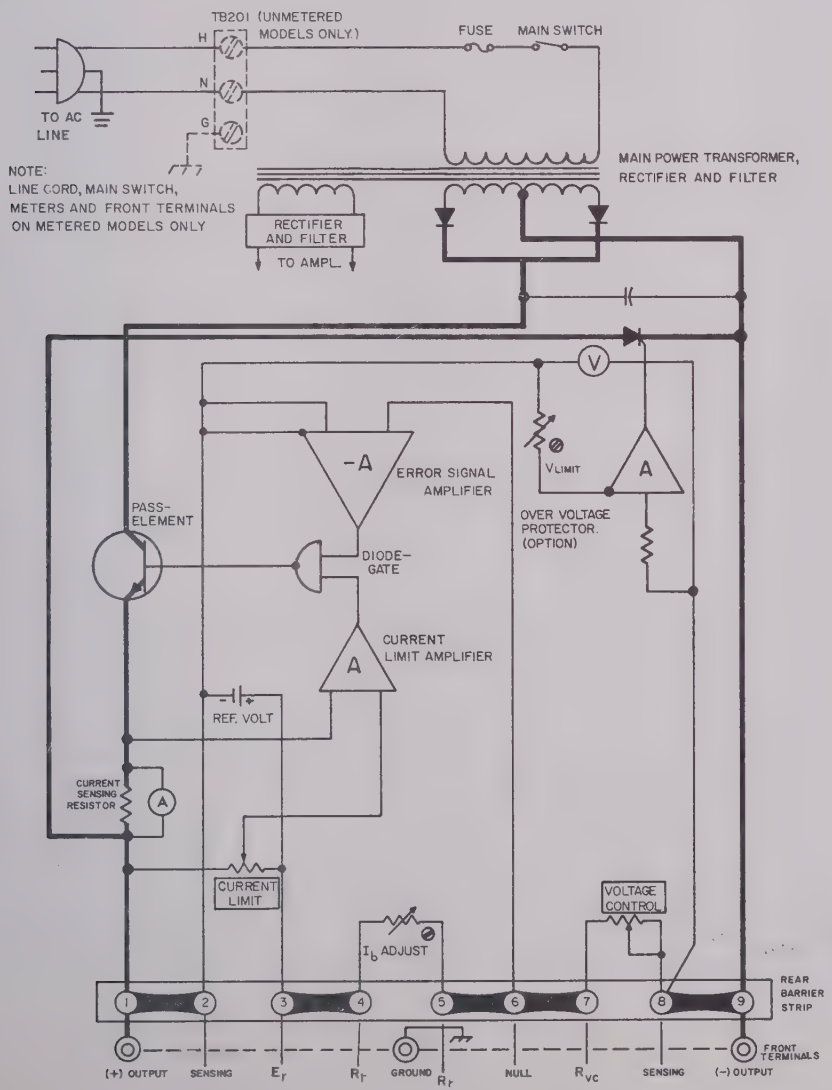


FIG. 4-1 JQE SIMPLIFIED DIAGRAM, AND REAR CONNECTIONS

4-4 The OVERVOLTAGE PROTECTOR (Optional, on models with suffix "VP" only.) is connected across the output terminals of the power supply. A portion of the output voltage is continuously compared to an internal reference. If an overvoltage occurrs, the S.C.R. "crowbar" will short-circuit the output immediately. Recycling takes place after the AC input voltage has been removed from the power supply.

4-5 CIRCUIT DESCRIPTION

4-6 For the purpose of analysis, the circuitry of the JQE power supply may be divided into several sections which are individually described below. The main schematic (FIG. 6-1) should be used to illustrate the text of this section.

a) AC INPUT CIRCUIT. AC line power is introduced through the (detachable) three-wire line cord with safety plug.* The use of a grounded AC power outlet will automatically ground the power supply, since the third wire of the line cord is directly connected to the metal chassis and case. Once the AC power switch (S101)* is closed, the primary of the main transformer (T201), the fan (B201) and the AC pilot light (DS101)* will be energized. The two primary windings of the main transformer are either connected in parallel (for 115V AC nominal line voltage) or in series (for 230V AC nominal line voltage). A slow-blow type fuse (F201)* protects the primary circuit from excessive current.

b) MAIN DC SUPPLY. The main DC power is derived from a center-tapped secondary winding on T201. A full-wave rectifier circuit with silicon diodes CR301, CR302 (located on the Heat Sink Assembly) works into a capacitor input filter (C201), paralleled by a bleeder resistor (R201). The main DC supply delivers the output current through the series regulator or pass elements.

c) SERIES REGULATOR (PASS-ELEMENTS). The series regulator transistors (Q301,302,303,304) are NPN silicon devices, located on a specially constructed heat sink (A3) and cooled by the fan (B201) Electrically, these pass transistors are connected in series with the (unregulated) main DC supply and the output. The effective series resistance of the pass transistors (and thereby the voltage drop across them) is changed in such a way as to keep the output voltage constant, regardless of variations in the unregulated main supply. The base drive needed to affect this change in the pass transistors is supplied by the main driver (Q305), also located on the heat sink assembly.

d) ERROR SIGNAL AMPLIFIER (A_V). The main function of this DC amplifier is to amplify the error signal, derived from the comparison bridge, to a level suitable to pass the diode gate circuit and drive the pre-driver stage (Q1). The error signal amplifier in the JQE power supply is a DC coupled, high gain operational amplifier in a plug-in package. The amplifier input may be disconnected from the comparison bridge circuit, and programmed externally. Although the plug-in amplifier is used in the non-inverting configuration, a positive input signal will produce a negative power supply output with respect to the "common" (plus (+) sensing terminal), since the pass-elements provide another signal inversion.

e) COMPARISON BRIDGE. This four-arm electrical bridge circuit is the sensing and controlling element in the JQE power supply. The reference-half of the bridge consists of the reference resistors in series with the zener reference voltage (R_r = R4, R5; E_r = CR201), the other half is constituted by the voltage control resistor (R_{VC} = R102) and the output voltage (E_O). The simplified presentation shown in Section III of this manual will illustrate this discussion and show the actual bridge configuration. (Refer to FIG. 3-4). The output voltage (in series with R_{VC}) is continuously compared with the reference half of the bridge circuit. At bridge balance ($E_{AA'} = 0$, a constant bridge current (I_b) is flowing through the bridge, keeping the error signal at the bridge terminals (A,A') at approximately zero volts. Any deviation in the output voltage (E_O), caused either by line/load variation or by a change in R_{VC} will tend to change the bridge current (I_b) in the sensing half of the bridge and thereby produce an error signal at the bridge terminals (A,A'). The error signal is presented to the amplifier input and will after amplification, act as a drive for the pass transistors, changing the voltage drop (E_p) across them in such a way as to restore bridge balance and keep the output voltage (E_O) constant once more.

f) CURRENT LIMIT CIRCUIT. The differential input of the current limit amplifier (Q3,Q4) is connected to the current sensing resistor (R_S = R307) and to a reference voltage, adjustable by the current limit control (R101). The voltage drop across the current sensing resistor is thus continuously compared to the reference voltage, set by the current limit control. As long as the voltage developed across R_S (due to load current flow) is less than the pre-set voltage reference level, the current limit amplifier will be biased to its inactive state and will not effect the output. If the output current increases however, the voltage drop across R_S will overcome the reference level set by the current limit control. The current limit amplifier will be thus activated and produce a drive signal at the diode gate, greater than A_V,

*These components on metered models (suffix "M") only. Line power on unmetered models is introduced via a rear barrier-strip (TB203). The grounding terminal on TB203 (G) should always be returned to AC ground.

thereby taking control away from the error signal amplifier and transferring the power supply into the current limit mode. Since both, the current limit amplifier and the error signal amplifier (A_V) are coupled through the diode gate circuit (CR13,14) to the pre-driver stage (Q1), the amplifier with the greater (negative) output will control the pass transistors and therefore the output.

g) OVERVOLTAGE PROTECTION CIRCUIT (Models with suffix "VP").

1) The overvoltage protector circuit contains its own reference voltage, to which a portion of the power supplies output voltage is continuously compared. In the balanced condition, the sensing voltage is approximately equal to the reference voltage and the circuit remains inactive. Should the sensing voltage, due to a rise in output voltage, at any time exceed the reference level, the Schmitt-Trigger stage (Q403,404) will produce a signal, which transfered through the emitter follower (Q402), will turn the driver stage (Q401) "on". The amplified signal passes through the coupling zener diode (CR402) to the gate of the "crowbar" (silicon controlled rectifier). Once the "crowbar" S.C.R. is fired, a short circuit is placed across the output. The main fuse will blow only if the cause of the overvoltage was a shorted element or a similar internal defect. If the overvoltage protector triggers due to other causes, the power supply will continue to operate into a short circuit until the cause for the overvoltage is removed and the protector circuitry is reactivated by momentarely switching the AC power "off".

2) Input and output connections for the over voltage protector (VP) are made via the printed circuit connector (P401) and its associated mating jack (J204). The VP contains its own auxiliary power supply, only the AC voltage is derived from a secondary winding on T201, and introduced via J204/P401. The AC is rectified by a conventional bridge rectifier (CR401) and capacitor filtered (C401). The rectified and filtered DC is zener regulated by CR404. The reference voltage is derived from the pre-regulated DC, thus providing a stable reference source (CR405).

3) The Schmitt-Trigger Stage (Q403,404) functions as a voltage comparator or sampling circuit by causing Q403 to change states as the base voltage of Q404 is altered. Since the collector voltage of Q404 is rigidly clamped to a stable reference, a positive base signal from the divider (due to overvoltage) will result in a change of stat in Q403, and subsequently in a signal to the coupling zener (CR402) and to the gate of the crowbar S.C.R. (CR403). The delay adjustment capacitor (C403) is an adjustable trim capacitor, its value determining the charging time constant at the output of the Schmitt-Trigger. In effect, the setting of the capacitor determines the "sensitivity" of the VP to overvoltage signals. Minimum capacity will yield fastest response times (and also be more sensitive to spurious triggering). The control is factory adjusted for the fastest response consistent with stable operation under all conditions.

4) The crowbar S.C.R. (CR403) is connected to the output, so that a short circuit is provided, once the device is turned "on" by the action described above. Change of state of the overvoltage protector will only take place, when the AC power (and thereby the operating voltage for the VP) is completely removed from the power supply.

h) AUXILIARY SUPPLIES

1) AMPLIFIER POWER SUPPLY. This full wave, bridge rectified source is derived from a secondary winding on the main transformer (T201), rectified by CR7, the supply is capacitor filtered by C7. A current regulator stage (Q2), followed by a string of zener diodes (CR9–12), provides the operating voltage for the error signal amplifier (A_V), the current limit amplifier stage (Q3,Q4) and the reference circuit (R4,R5,CR201).

2) PRE-DRIVER COLLECTOR SUPPLY. A half wave rectified (CR3) and capacitor filtered (C2) supply is derived from a secondary winding on T201. It delivers collector voltage to the pre-driver stage (Q1). R3 is a bleeder and the network consisting of R24, C12, provides local feedback for the pre-driver stage.

3) I_{CBO} SUPPLY. This full-wave rectified auxiliary supply is derived from the winding of the main power supply. Rectified by CR1 and CR2, and filtered by C1, the voltage is applied to the base of the main driver (via R1) and the pass transistors (via R202), thus insuring the necessary turn-off bias for these stages under all operating conditions, especially at elevated temperatures.

SECTION V – MAINTENANCE

5-1 GENERAL

5-2 This section covers maintenance procedures, installation of optional components, calibration and test measurements of the Kepco Model JQE Power Supplies. Conservative rating of components and the non-congested lay out should keep maintenance problems to a minimum. If trouble does develop however, the easily removed wrap-around cover and the plug-in feature of the circuit boards provide exceptional accessibility to all components of the supply.

5-3 DISASSEMBLY (Refer to FIG. 5-1)

a) COVER REMOVAL. The wrap-around cover may be taken off by loosening and removing its nine (9) holding screws, three (3) on each side and two (2) on the front panel.

b) CIRCUIT BOARD REMOVAL. The printed circuit board is mounted with three screws, two (2) of which are removed from the rear, one (1) from the side. After removal of the two printed circuit board connectors, the board may be lifted from its slide-guide.

c) HEAT SINK REMOVAL. The heat sink assembly is mounted with two (2) screws to the chassis bottom. After the screws are taken out, the wires may be removed by means of the "quick disconnect" termianls.

d) RE-ASSEMBLY. Re-assembly of all components takes place in reverse order of the above described procedures.

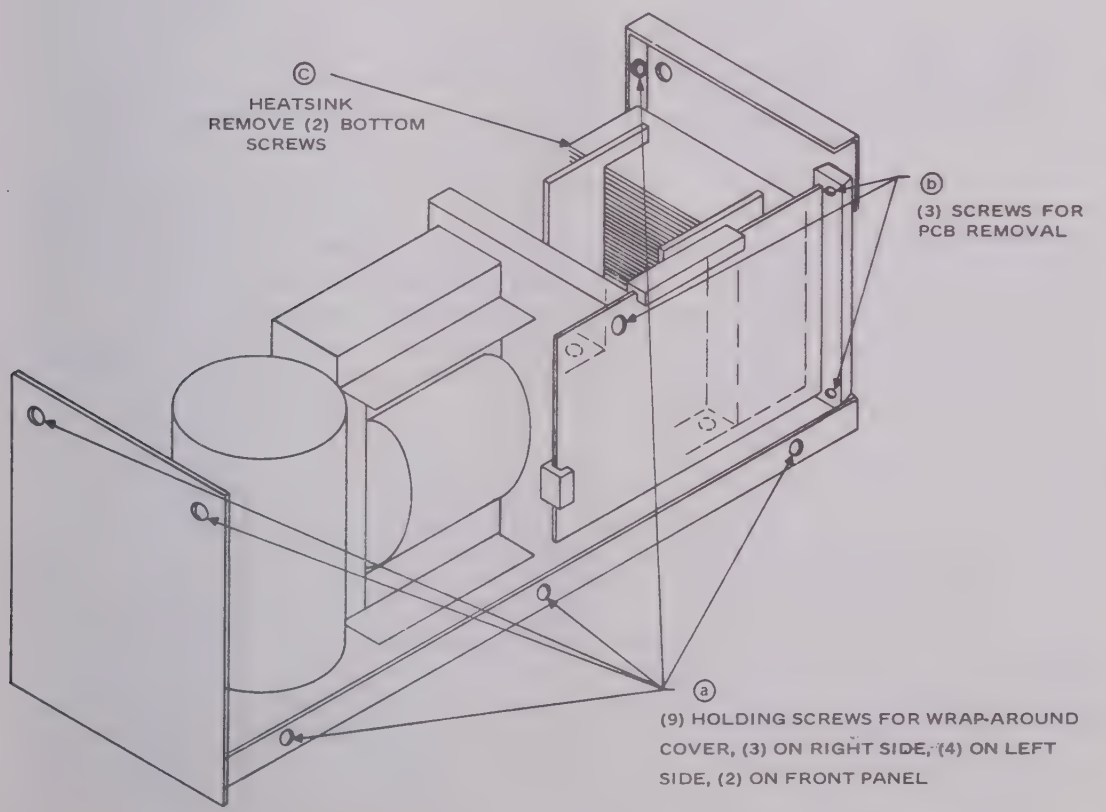


FIG. 5-1 DISASSEMBLY, JQE DESIGN GROUP

5-4 INTERNAL ADJUSTMENT PROCEDURES

5-5 GENERAL. This paragrpah describes all internal adjustment procedures, which have not been previously covered. Refer to Section II, paragraph 2-3, for a listing and location of all adjustment procedures.

5-6 MAXIMUM OUTPUT CURRENT ("I_O max. adj.") ADJUSTMENT. This adjustment serves as a calibration for the range of the front panel Current Limit Control. The "I_O max. adj." (R19) is located on the printed circuit board (Refer to FIG. 2-1). It is factory set so that the front panel control covers an output current range of 10 – 105% of the maximum rated output current. Re-adjustment of the "I_O max. adj." control is needed only if a component in the current limit circuitry must be replaced or, if the range of the front panel control is to be restricted. A convenient way to make the adjustment is as follows:

a) Load the output of the supply to draw the maximum desired output current with the front panel Current Limit Control fully clockwise. Monitor the output voltage with an oscilloscope, having a vertical sensitivity of at least 0.1 mV/cm.

b) Observe the oscilloscope. Current limiting is indicated by a sharp increase in output ripple. Locate the "I_O max. adj." control and turn slowly until limiting occurs at the point required. Clockwise adjustment will increase the maximum output current range, while counter clockwise adjustment will decrease the range. DO NOT ADJUST TO MORE THAN 105% I_O max. Although this might be possible due to component tolerances, malfunction of the power supply will be caused by exceeding the rated maximum output current.

5-7 LAG NETWORK ADJUSTMENTS. An AC stability control in the form of a lag network has been provided in this power supply (R16, see FIG. 2-1 for location). This network has been factory adjusted for maximum amplifier stability when operating into a resistive load. Re-adjustment is indicated if components, affecting the AC characteristics of the amplifier must be replaced or, if the load connected to the power supply contains excessive capacity or inductance, causing AC instability. AC instability is usually indicated by high frequency oscillation as observed with an oscilloscope across the power supply load. In such cases, the lag network should be adjusted so that stable operation is resumed. If in extreme cases adjustment of the lag network should not prove sufficient, twisted load and error sensing wires or decoupling capacity directly across the load may provide a solution to the problem.

5-8 INSTALLATION AND ADJUSTMENT OF THE "ZERO" CONTROL

NOTE: Models which have this control factory installed carry the suffix "E" behind their model number (i.e. JQE 6–10ME). The installation instructions may be disregarded with these models.

5-9 A small (5–15 mV), negative offset voltage exists at the output of the power supply, with the voltage control set to "zero" (fully counter clockwise). Although this does not affect standard power supply operation, for precision remote programming, exact zero volt output may be necessary. The zero can be established by adding the necessary components on a pre-designated space on the printed circuit board.

5-10 The installation may be readily accomplished by following the steps indicated below:

a) Remove the PCB Assembly from the chassis as described in paragraph 5-3. Locate the pre-designated space for the additional components on FIG. 5-2.

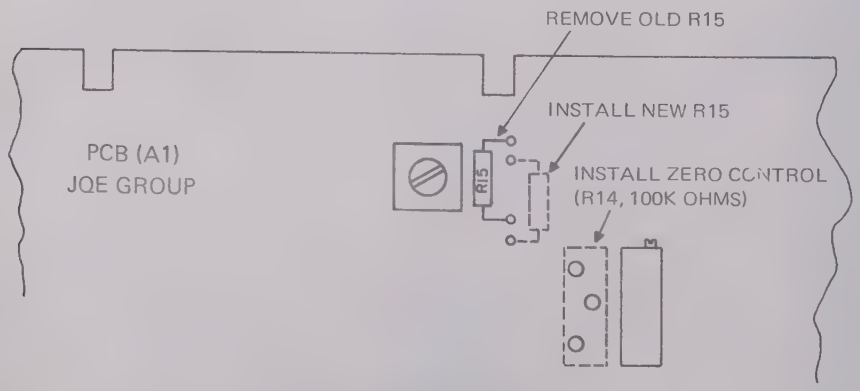


FIG. 5-2 INSTALLATION OF ZERO CONTROL

b) Observing the standard precautions for soldering on printed circuit boards (Iron not above 60 W, No acid solder etc.) remove R15 (499 K) and replace with new value (10 ohm, metal film). Install 100K metal film rheostat into the space shown and solder components securely into place. This concludes the installation. Adjust precision zero as directed in paragraph 3-12 (Adjustments for Precision Programming Ratio).

5-11 TROUBLE SHOOTING

5-12 Modern, high performance power supplies have reached a state of sophistication that requires thorough understanding of the problems involved in repairing complex, solid state circuitry. Servicing beyond simple parts replacements should consequently be attempted only by personnel thoroughly familiar with solid state component techniques and with experience in closed loop circuitry.

5-13 Trouble-shooting charts, showing resistance and voltage readings, are of very limited usefulness with feedback amplifiers and are not included here. Instead a detailed circuit description (Section IV), parts location diagrams, simplified functional schematics and a main schematic with all significant voltage readings is presented.

5-14 The following basic steps in a case of power supply malfunctioning may also be found useful:

a) If power supply does not function at all: Check all powerline connections and fuses and make certain, power supply is connected to the correct line voltage (it is normally delivered for 115V AC operation).

b) If supply does not function in one of the remote programming modes, disconnect all external components and reconnect internal reference and the voltage control. The following paragraphs refer to the instrument as a power supply, rather than an amplifier in order to simplify test set-ups and measurements.

c) If the power supply is basically functioning, but poor performance is evident, inspect the test set-up to make certain the source of the trouble is not external.

d) Poor regulation in any of the regulating modes is usually traceable to incorrectly connected loads or faulty measurement techniques. Perform measurements as described in Paragraph 5-16 and follow the connecting diagrams.

e) Oscillation of the output voltage or output current is often due to a load with a large inductive component. Twisted loadwires of sufficient diameter and held as short as practical, are often the solution to the problem, if lag network adjustment does not correct the trouble. (Refer to par. 5-7).

5-15 KEPCO Field Engineering Offices or the KEPCO Repair Department will be available for consultation or direct help in difficult service or application problems.

5-16 POWER SUPPLY MEASUREMENTS

5-17 Measurement of the significant parameters of a power supply is a requirement for incoming inspection, periodic maintenance, or after component replacement. Since the measurements require special techniques to insure correct results, suggestions for Their performance are given below:

a) Require Instrumentation:

1) Constant AC supply voltage with provisions for "stepping" the voltage over a specified region (105-125V); a variable autotransformer is generally adequate, if it is rated to deliver the input current of the unit under test.

2) Resistive load, variable, with ON/OFF and SHORTING SWITCH and capable of dissipating the full output power of the unit under test.

3) DC voltage monitor, differential voltmeter, or power supply analyzer.

4) Current sensing resistor, for current regulation measurements, four-terminal device.

5) AC ripple monitor, sensitivity better than 1 mV. Ballantine Model 302C, or Hewlett Packard Model 400H.

6) Optional: Oscilloscope, vertical sensitivity better than 0.1 mV/cm.

b) FIG. 5-3 shows the proper location of instrument leads when measuring regulation. The principle governing this method of measurement is simply not measure any voltage drops due to load current flow. This can only be avoided by measuring regulation either directly at the sensing leads or by the use of four-terminal network. The four-terminal network can be approximated by the method shown in FIG. 5-3d.

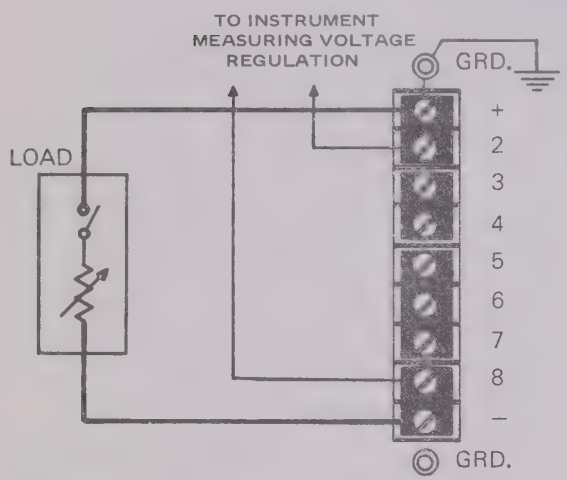
c) Voltage Regulation is defined as the amount of output voltage change resulting from a specified change of line voltage or from a change in load resistance. It can be expressed as an absolute change ΔE_O or as a percentage in reference to the total output voltage E_O,:

$$\% \text{ Voltage Regulation} = \frac{\Delta E_O}{E_O} \quad X \quad 100\%.$$

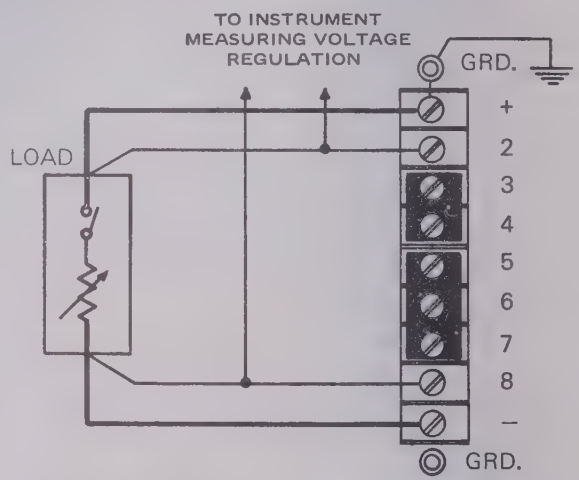
d) Current Regulation is defined as the amount of output current change resulting from a specified change in line voltage or from a change in load resistance. It can be expressed as an absolute change ΔI_O, or as a percentage in reference to the total output current I_O,:

$$\% \text{ Current Regulation} = \frac{\Delta I_O}{I_O} \quad X \quad 100\%.$$

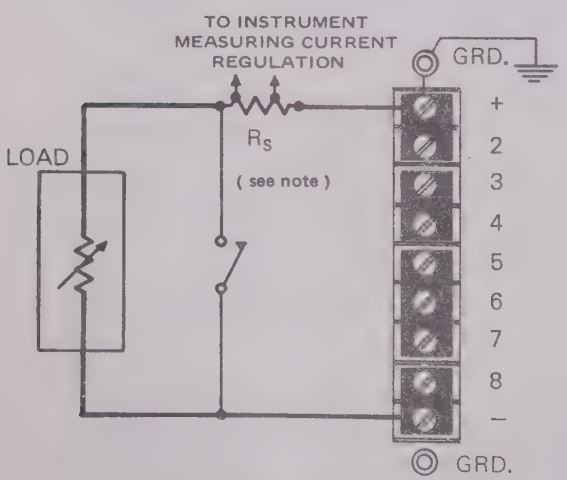
e) Ripple, RMS ripple may be monitored on a true RMS reading instrument connected parallel to the regulation analyzer leads. Careful wire dressing and shielding, as well as good AC grounding are of the utmost importance if valid measurements are being expected. An oscilloscope may also be used for P-P readings of noise and ripple. An approximate RMS reading can be calculated from the P-P reading of the oscilloscope if the reading is divided by three.



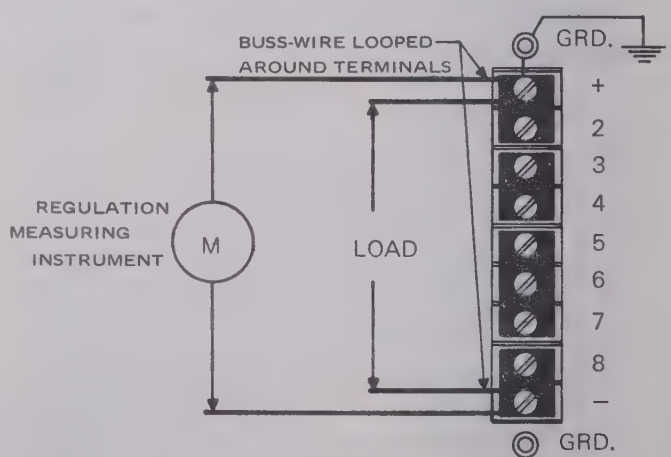
A) REGULATION MEASUREMENT (VOLTAGE) WITHOUT ERROR SENSING AT THE LOAD



B) REGULATION MEASUREMENT (VOLTAGE) WITH ERROR SENSING AT THE LOAD



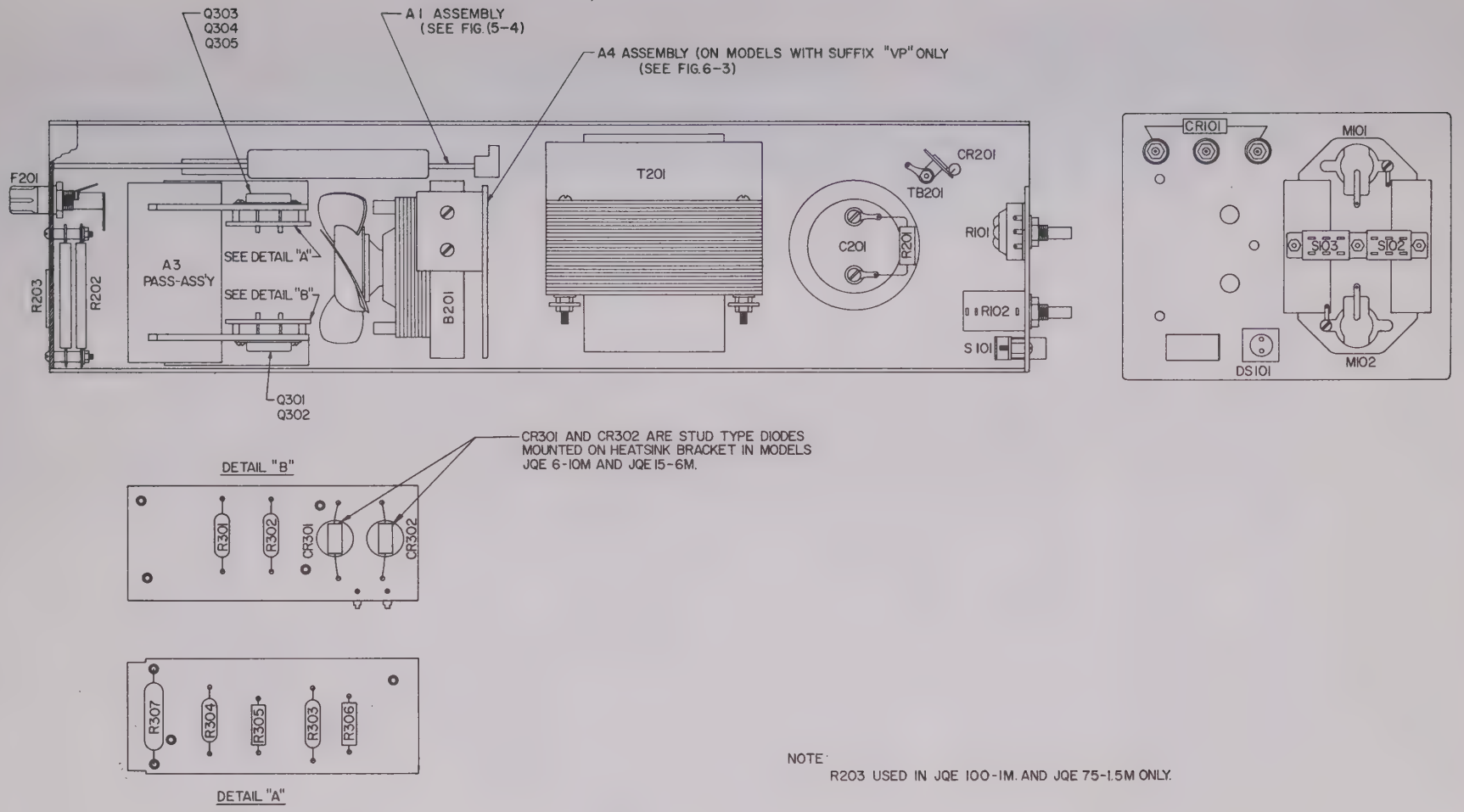
C) REGULATION MEASUREMENT (CURRENT)



D) APPROXIMATION OF A FOUR–TERMINAL NETWORK NETWORK

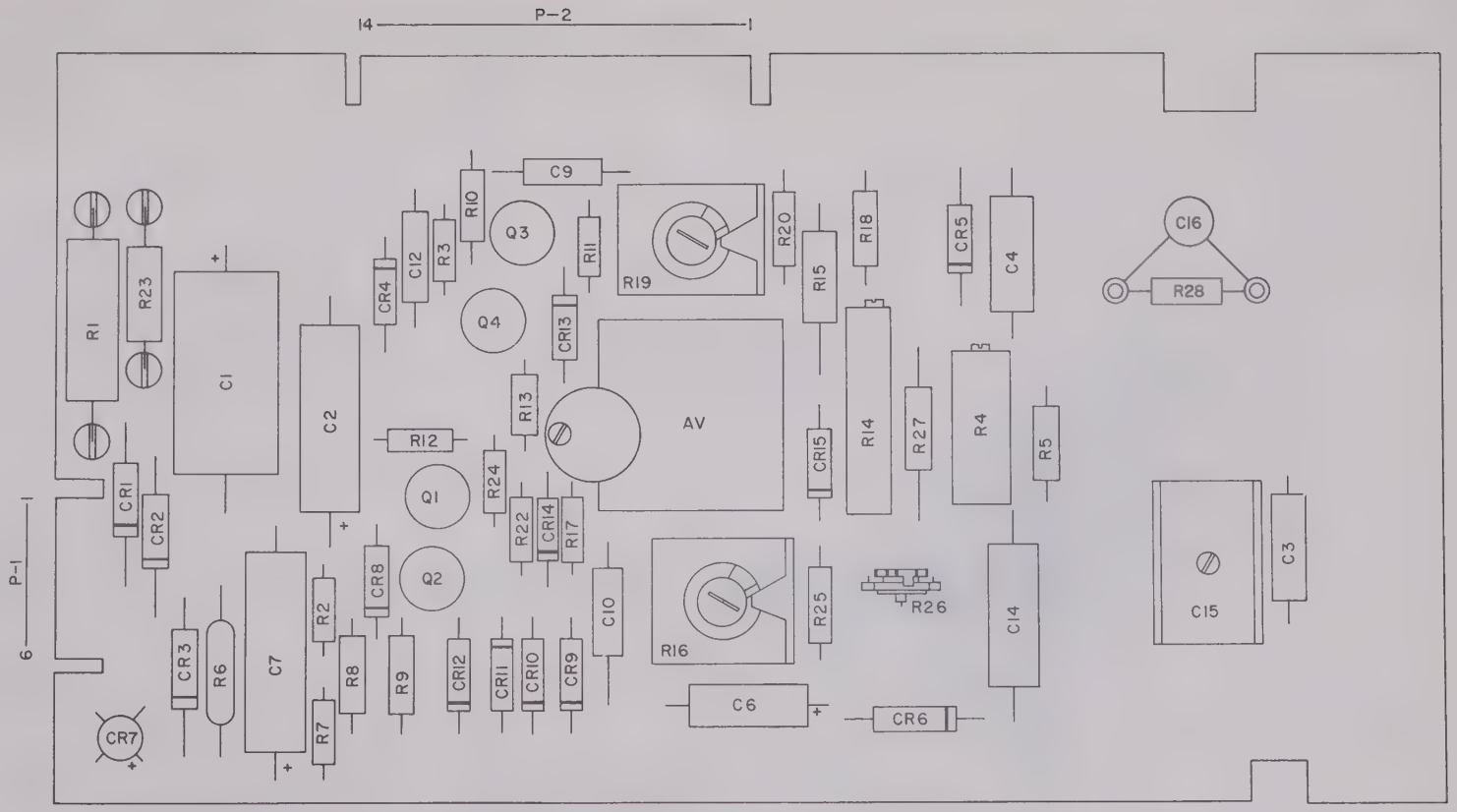
*NOTE: R_S should be selected such that its wattage rating is at least 10 times the actual power dissipated. Stability and regulation depend on the stability of R_S. Recommended T.C. for R_S is 20 ppm.

FIG. 5-3 CONNECTIONS FOR POWER SUPPLY MEASUREMENTS



NOTE:
R203 USED IN JQE 100-1M. AND JQE 75-1.5M ONLY.

FIG. 5-4 MAIN CHASSIS, COMPONENT LOCATION



NOTES.

1. C12 USED ON JQE 100-1M HS AND JQE55-2M HS ONLY.
2. C3 NOT USED ON JQE 100-1M HS, JQE75-1.5M HS AND JQE36-3M HS.
3. R22 NOT USED ON JQE36-3M HS AND JQE25-4M HS.
4. C16 USED ON JQE 75-1.5M HS, JQE15-6M HS AND JQE6-10M HS ONLY.
5. R28 USED ON JQE75-1.5M HS, JQE15-6M HS AND JQE6-10M HS ONLY.

FIG. 5-5 PRINTED CIRCUIT BOARD, COMPONENT LOCATION (HS)

SECTION VI – ELECTRICAL PARTS LIST AND DIAGRAMS

6-1 GENERAL

6-2 This section contains the main schematic, the parts location diagrams, and a list of all replaceable electrical parts. All components are listed in alpha-numerical order of their reference designations. Consult your Kepco Representative for replacement of parts not listed here.

6-3 ORDERING INFORMATION

6-4 To order a replacement part or to inquire about parts not listed in the parts list, address order or inquiry either to your authorized Kepco Sales Representative or to:

KEPCO, INC.
131–38 Sanford Avenue
Flushing, N.Y. 11352

6-5 Specify the following information for each part:

a) Model and complete serial number of instrument.

b) Kepco part number.

c) Circuit reference designator.

d) Description.

6-6 To order a part not listed in the parts list, give a complete description and include its function and location.

PRINTED CIRCUIT BOARD ASSEMBLY (A1),MODEL JQE 36–3(M)HS (T) Code 4-969

SCHEMATIC NO.	QTY.	DESCRIPTION	MFRS. NAME & PART NO. (SEE NOTE)	KEPCO PART NO.
A_V	1	Plug-In Amplifier	Kepco Inc. 250-0013	250-0013
C1	1	Capacitor,Electrolytic,Pigtail 75 μF, 75 V	Sprague 38D756G075FE2	117-0622
C2	1	Capacitor,Electrolytic,Pigtail 290 μF, 12 V	Sprague 30D297G012DF2	117-0647
C4,6	2	Capacitor,Electrolytic,Pigtail 150 μF, 3 V	General Electric 76F02BF151	117-0512
C7	1	Capacitor,Electrolytic,Pigtail 75 μF, 50 V	Sprague 30D756G050DF2	117-0646
C9	1	Capacitor,Mylar,Pigtail 0.01 μF, 200 V	Sprague 192P10302	117-0353
C10	1	Capacitor,Mylar,Pigtail 5000 pF, 500 V	RMC Type 5M	117-0061
C14	1	Capacitor,Mylar,Pigtail 1 μF, 200 V	Wesco 33MM	117-0207
C15	1	Capacitor,Variable 300-1200 pF, 175 V	Arco PC4613	117-0572
CR1,2,15	3	Rectifier,Silicon,Pigtail	Semicon SM-140	124-0028
CR3,4,13, 14	4	Rectifier,Silicon,Pigtail	Semicon SM-110	124-0133
CR5,6	2	Rectifier,Silicon,Pigtail	Solitron 124-0178	124-0178
CR7	1	Rectifier,Silicon,Bridge,Pigtail	Varo EBR-W102	124-0346
CR8,9,10, 11,12	5	Zener Diode	Transitron SV125	121-0028
Q1,2	2	Transistor,Silicon,NPN	M.S. Transistor 119-0094	119-0094
Q3,4	2	Transistor,Silicon,NPN	General Electric 2N336A	119-0056
R1	1	Resistor,Fixed,Molded 7.5 K ohm, 2 W, 5%	Allen Bradley HB7525	115-2119
R2	1	Resistor,Fixed,Molded 27 ohm, 1/4 W, 10%	IRC GBT 1/4	115-2317
R3	1	Resistor,Fixed,Molded 560 ohm, 1/4 W, 10%	IRC GBT 1/4	115-2210
R4	1	Resistor,Variable,WW 1 K ohm, 0.5 W, 10%	Bourns 3067S-1-102	115-0739
R5	1	Resistor,Fixed,Precision,WW 5.7 K ohm, 1 W, 1%	IRC CEA Type	115-1301
R6	1	Resistor,Fixed,Precision,M.F. 232 ohm, 1 W, 1%	IRC CES Type	115-1953
R7	1	Resistor,Fixed,M.G. 82 K ohm, 1/4 W 5%	IRC RG 1/4	115-2012

NOTE:

REPLACEMENT PARTS MAY BE ORDERED FROM KEPCO, INC. ORDERS SHOULD INCLUDE KEPCO PART NUMBER AND DESCRIPTION.

PLEASE NOTE: THE MANUFACTURER'S NAME AND PART NUMBER LISTED FOR EACH ITEM ON REPLACEMENT PARTS LISTS, REPRESENTS AT LEAST ONE SOURCE FOR THAT ITEM AND IS LISTED SOLELY FOR THE CONVENIENCE OF KEPCO EQUIPMENT OWNERS IN OBTAINING REPLACEMENT PARTS LOCALLY. WE RESERVE THE RIGHT TO USE EQUIVALENT ITEMS FROM ALTERNATE SOURCES. KEPCO, INC.

PRINTED CIRCUIT BOARD ASSEMBLY (A1),MODEL JQE 36–3(M)HS(T) Code 4-969

SCHEMATIC NO.	QTY.	DESCRIPTION	MFRS. NAME & PART NO. (SEE NOTE)	KEPCO PART NO.
R8	1	Resistor,Fixed,Molded 3.9 K ohm, 1/2 W, 5%	Allen Bradley EB3925	115-0808
R9	1	Resistor,Fixed,Molded 1 K ohm, 1/2 W, 5%	Allen Bradley EB1025	115-0340
R10	1	Resistor,Fixed,M.G. 510 ohm, 1/4 W, 5%	IRC RG 1/4	115-2274
R11	1	Resistor,Fixed,Molded 47 K ohm, 1/4 W, 10%	IRC GBT 1/4	115-2237
R12	1	Resistor,Fixed,M.G. 6.2 K ohm, 1/4 W, 5%	IRC RG 1/4	115-2258
R13	1	Resistor,Fixed,Molded 12 K ohm, 1/4 W, 10%	IRC GBT 1/4	115-2276
R14	1	Resistor,Variable,Cermet 100 K ohm, 3/4 W, 20%	Helipot 78PR100K	115-2044
R15	1	Resistor,Fixed,Molded 10 ohm, 1/2 W, 10%	Allen Bradley EB1001	115-0520
R17	1	Resistor,Fixed,Molded 1.5 K ohm, 1/4 W, 10%	IRC GBT 1/4	115-2229
R18	1	Resistor,Fixed,Precision,M.F. 750 ohm, 1/4 W, 1%	IRC CEA Type	115-2259
R19	1	Resistor,Variable,Composition 20 K ohm, 1/4 W, 30%	C.R.L. Type 71-1	115-2189
R20	1	Resistor,Fixed,Precision,M.F. 10 K ohm, 1/4 W, 1%	IRC CEA Type	115-2174
R23	1	Resistor,Fixed,Molded 5.6 K ohm, 1 W, 10%	Allen Bradley GB5621	115-0546
R24	1	Resistor,Fixed,M.G. 750 ohm, 1/4 W, 5%	IRC RG 1/4	115-2352
R25	1	Resistor,Fixed,Molded 2 K ohm, 1/2 W, 5%	Allen Bradley EB2025	115-0520
R26	1	Resistor,Variable,Composition 100 ohm, 1/4 W, 30%	C.T.S. X201	115-1940
R27	1	Resistor,Fixed,Precision 100 ohm, 1 W, 1%	IRC CES Type	115-1846

NOTE:

REPLACEMENT PARTS MAY BE ORDERED FROM KEPCO, INC. ORDERS SHOULD INCLUDE KEPCO PART NUMBER AND DESCRIPTION.

PLEASE NOTE: THE MANUFACTURER'S NAME AND PART NUMBER LISTED FOR EACH ITEM ON REPLACEMENT PARTS LISTS, REPRESENTS AT LEAST ONE SOURCE FOR THAT ITEM AND IS LISTED SOLELY FOR THE CONVENIENCE OF KEPCO EQUIPMENT OWNERS IN OBTAINING REPLACEMENT PARTS LOCALLY. WE RESERVE THE RIGHT TO USE EQUIVALENT ITEMS FROM ALTERNATE SOURCES. KEPCO, INC.

REPLACEMENT PARTS LIST

MAIN CHASSIS ASSEMBLY (A2),MODEL JQE 36–3(M)HS (T) **Code 4-969**

SCHEMATIC NO.	QTY.	DESCRIPTION	MFRS. NAME & PART NO. (SEE NOTE)	KEPCO PART NO.
FRONT PANEL ASSEMBLY CONTAINS:				
DS101	1	Pilot Light Assembly	Industrial Devices 2100 Series	152-0087
CR101	1	Rectifier,Silicon,Pigtail	Semicon SM-140	124-0028
*M101	1	Meter 0–3 Amps.	Honeywell Model 504	135-0418
*M102	1	Meter 0–40 Volt	Honeywell Model 504	135-0448
R101	1	Resistor,Variable,Composition 5 K ohm, 1/2 W	C.T.S. VA-45	115-2277
R102	1	Resistor,Variable,WW 40 K ohm, 2 W, 5%	IRC Series 8000	115-1238
*S102,103	2	Switch	Stackpole SS-72-1	127-0254
B201	1	Motor	Howard Industries 21075	148-0026
C201	1	Capacitor,Electrolytic,Pigtail 10.4 K μF, 75 V	General Electric 86F1126MA	117-0628
CR201	1	Zener Diode	International Rect. 1N821	121-0041
*CR201	1	Zener Diode	IRC 1N827	121-0062
F201	1	Fuse,Slow Blow 2.5 A	Buss MDL-2 1/2	141-0029
R201	1	Resistor,Fixed,Molded 2 K ohm, 2 W, 5%	Allen Bradley HB2025	115-0074
R202	1	Resistor,Fixed,Power,Strip 1 K ohm, 20 W, 5%	Hardwick Hindle B459-1K	115-0611
S201	1	Switch	Stackpole RS47-FB	127-0230
T201	1	Transformer	Kepco Magnetics	100-1760

*Metered Models Only.

*On Models with "T" Only.

NOTE:

REPLACEMENT PARTS MAY BE ORDERED FROM KEPCO, INC. ORDERS SHOULD INCLUDE KEPCO PART NUMBER AND DESCRIPTION.

PLEASE NOTE: THE MANUFACTURER'S NAME AND PART NUMBER LISTED FOR EACH ITEM ON REPLACEMENT PARTS LISTS, REPRESENTS AT LEAST ONE SOURCE FOR THAT ITEM AND IS LISTED SOLELY FOR THE CONVENIENCE OF KEPCO EQUIPMENT OWNERS IN OBTAINING REPLACEMENT PARTS LOCALLY. WE RESERVE THE RIGHT TO USE EQUIVALENT ITEMS FROM ALTERNATE SOURCES. KEPCO, INC.

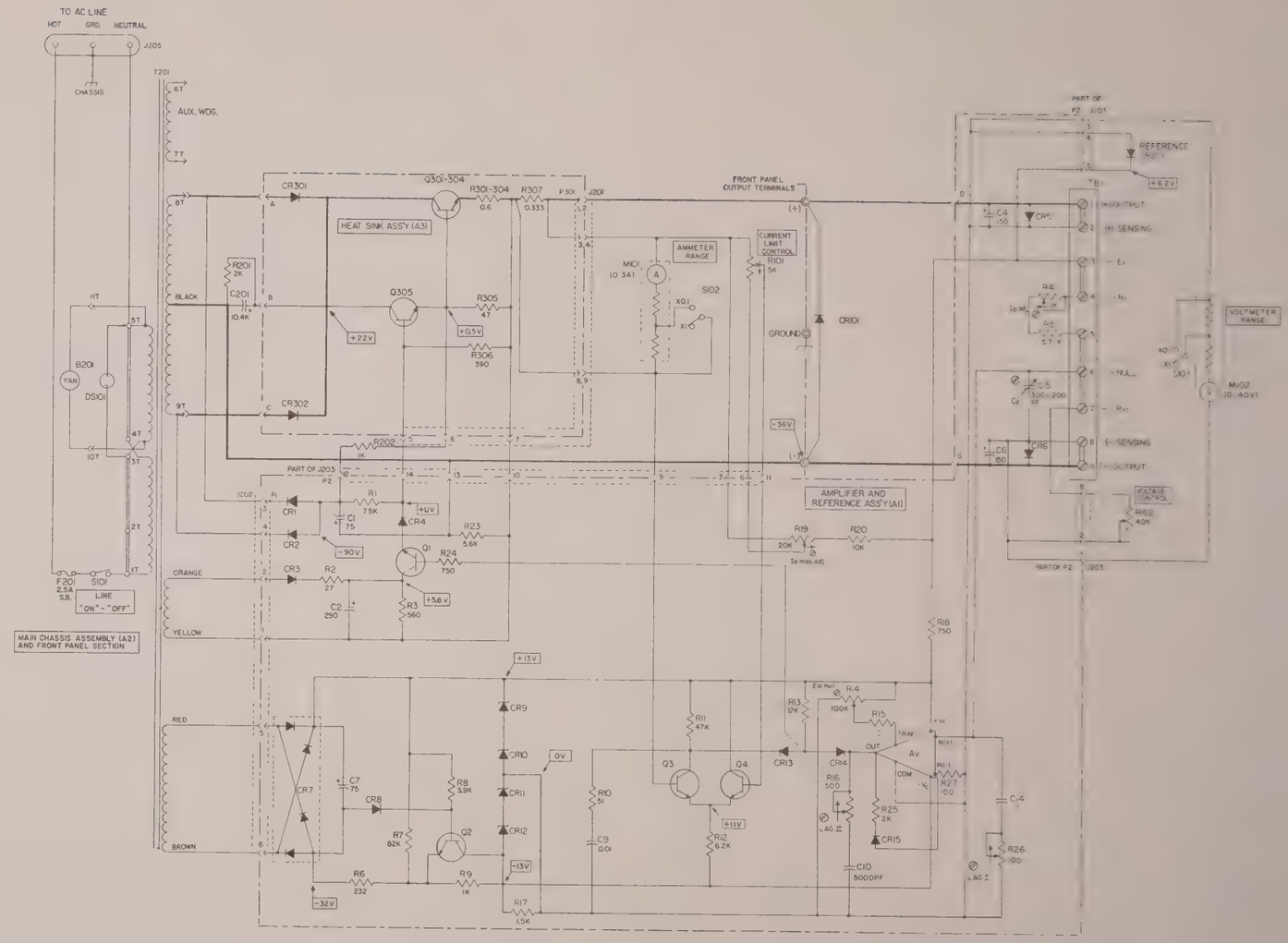
HEATSINK ASSEMBLY (A3),MODEL JQE 36–3(M)HS (T) **Code 4-969**

SCHEMATIC NO.	QTY.	DESCRIPTION	MFRS. NAME & PART NO. (SEE NOTE)	KEPCO PART NO.
CR301,302	2	Rectifier,Silicon	Semicon S-5A4	124-0348
Q301,302, 303,304	4	Transistor,NPN	RCA 2N3055	119-0061
Q305	1	Transistor,NPN	RCA 2N3054	119-0060
R301,302 303,304	4	Resistor,Fixed,Power,Axial 0.6 ohm, 3 W, 3%	Tepro TS-3W	115-2138
R305	1	Resistor,Fixed,Molded 47 ohm, 1/4 W, 10%	IRC GBT 1/4	115-2273
R306	1	Resistor,Fixed,Molded 390 ohm, 1/4 W, 10%	IRC GBT 1/4	115-2234
R307	1	Resistor,Fixed,Precision 0.333 ohm, 5 W, 1%	Tepro TSK-5W	115-2197

NOTE:

REPLACEMENT PARTS MAY BE ORDERED FROM KEPCO, INC. ORDERS SHOULD INCLUDE KEPCO PART NUMBER AND DESCRIPTION.

PLEASE NOTE: THE MANUFACTURER'S NAME AND PART NUMBER LISTED FOR EACH ITEM ON REPLACEMENT PARTS LISTS, REPRESENTS AT LEAST ONE SOURCE FOR THAT ITEM AND IS LISTED SOLELY FOR THE CONVENIENCE OF KEPCO EQUIPMENT OWNERS IN OBTAINING REPLACEMENT PARTS LOCALLY. WE RESERVE THE RIGHT TO USE EQUIVALENT ITEMS FROM ALTERNATE SOURCES. KEPCO, INC.



NOTES:
1. RESISTOR VALUES IN OHMS, K=1000
2. CAPACITOR VALUES IN MICROFARADS, UNLESS INDICATED OTHERWISE.
3. CONNECTORS MARKED WITH CAPITAL LETTERS ARE "QUICK-DISCONNECT" TERMINALS. CONNECTORS SUFFIXED "T" ARE TRANSFORMER TERMINALS.
4. VOLTAGE READINGS TAKEN UNDER THE FOLLOWING CONDITIONS:
   A) LINE AT 115VAC, Eo= MAX. RATED, Io=0.
   B) METER, 20K OHM / VOLT
   C) ALL VALUES WITH REFERENCE TO THE POSITIVE (+) SENSING TERMINAL.

kepco Data subject to change without notice
PATENT NOTICE: Applicable Patent. Nos. ● will be supplied on request.

FIG. 6-1 MAIN SCHEMATIC, MODEL JQE 36-3(M)HS

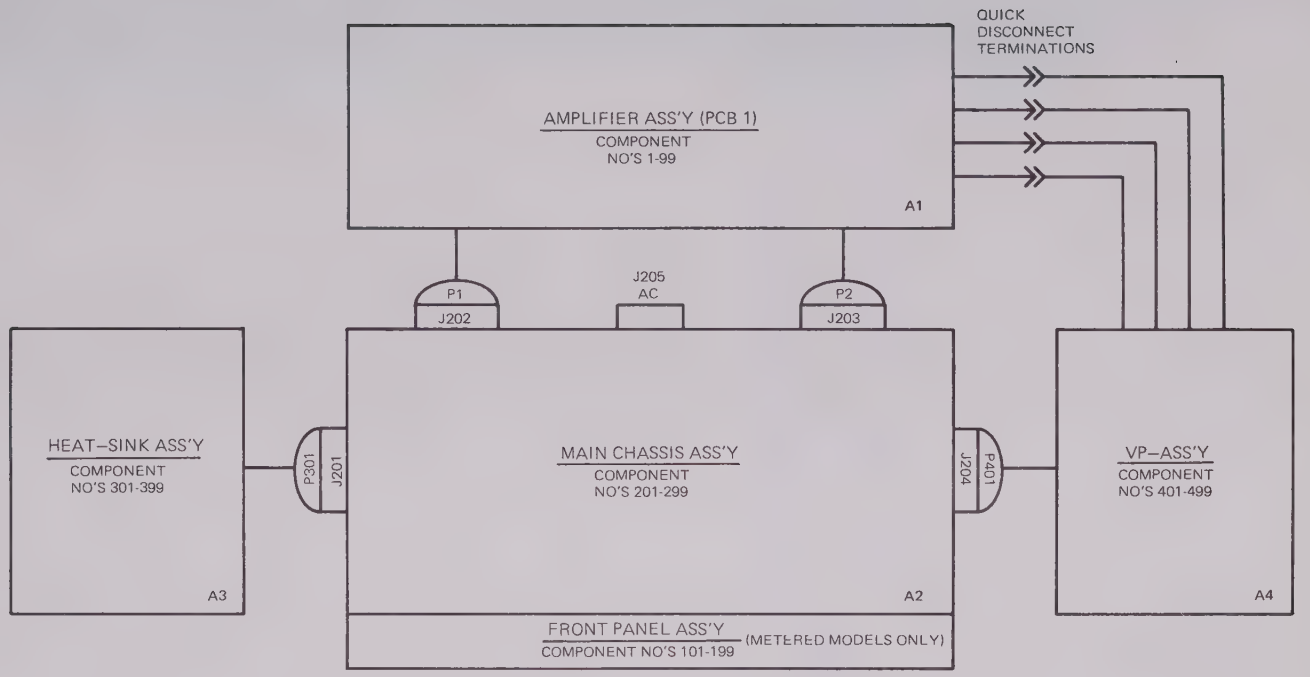


FIG. 6-2 PLUG-IN DIAGRAM

ADDENDUM TO STANDARD MANUAL

FOR KEPCO JQE–HS SERIES,HIGH–SPEED

OPERATIONAL POWER SUPPLIES

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SECTION I – INTRODUCTION

1-1 SCOPE OF ADDENDUM

1-2 This addendum to the standard manual for the JQE series of power supplies contains instructions for operation, test and alignment of the JQE–HS group of high speed operational power supplies.

1-3 GENERAL DESCRIPTION

1-4 The Kepco JQE–HS Design Group represent a series of high speed operational power supplies derived from the standard JQE models. The major difference between the standard and the high speed version of the JQE is the greatly increased programming or slewing speed of the latter. While on standard JQE supplies the programming speed is limited to approximately 200 to 500 volts per second, the high speed model achieves slewing rates in excess of 100,000 V/Sec. The high programming speed is made possible by eliminating the output capacitor and stabilizing the amplifier by the addition of appropriate lag components. The new high speed circuitry emphasizes the amplifier rather than the power supply characteristics of the instrument. For the vastly increased bandwidth of the high speed operational supply, slightly higher output noise and ripple as well as limited capacitive loading must be accepted.

1-5 SPECIFICATIONS, JQE–HS GROUP

1-6 Specifications as presented for the standard JQE Model are valid, with the following exceptions and additions:

a) OFFSETS: (Refer to par. 3-3 for definitions)

PARAMETER	AMPLIFIER OFFSETS		INTERNAL VOLTAGE(6.2V) REFERENCE
	ΔE_{io}	ΔI_{io}	
LINE: (105-125 or 210-250 V AC	$<10\,\mu V$	<2 nA	0.0001%
LOAD: (No Load/Full Load)	$<200\,\mu V$	<5 nA	
TIME: (8 Hrs.)	$<20\,\mu V$	<2 nA	0.005%
TEMP.: (per $^{\circ}C$)	$<20\,\mu V$	<5 nA	0.005%

b) RIPPLE: $<0.01\%$ of $E_o$max. or 3 mV (rms) whichever is greater.

c) MAXIMUM CAPACITIVE LOADING: 0.001 μF.

d) PROGRAMMING SPEED: $>100{,}000$ V/sec.

e) GAIN: (open loop) : $>0.5 \times 10^6$ volts per volt.

f) OUTPUT IMPEDANCE: Add 100 μH source inductance to DC specification of standard model.

1-7 TERMINATIONS AND CONTROLS

1-8 Additional internal controls have been added to the high speed model of the JQE–HS series. A summary of ALL controls and their functions is presented in the table below. Their location is indicated in FIG. 1-1.

REFERENCE DESIGNATION	INTERNAL CONTROL	FUNCTION	ADJUSTMENT PROCEDURE SEE:
R4	I_b adj.	Control Current Calibration	Par. 3-16 (Std. Manual)
R14	E_{io} Null	Offset Nulling	Par. 3-16 (Std. Manual)
R16	Lag Adj. (LAG I)	AC Stability Adjustment	Par. 2-3 (Addendum)
R24	Lag Adj. (LAG II)	AC Stability Adjustment	Par. 2-3 (Addendum)
C13	Feedback Capacitor (C_f)	AC Stability Adjustment	Par. 2-3 (Addendum)

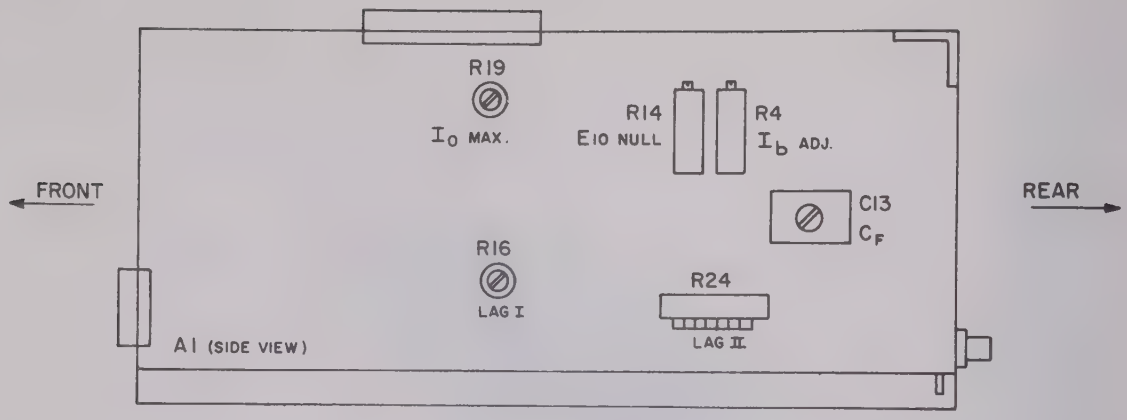


FIG. 1-1 LOCATION OF INTERNAL CONTROLS

SECTION II – OPERATION

2-1 GENERAL

2-2 All general operating procedures and programming methods described in Section III of the standard manual are applicable for the JQE High Speed Operational Power Supplies. In addition, all programming can be performed with a speed of at least 100,000 Volts per second. The sine wave frequency response is illustrated in FIG. 2-3. The chart shows the sinusoidal excursions (Maximum rated output voltage, peak to peak) plotted against the frequency with several slewing rates as a running parameter. For the JQE–HS, the diagonal line marked "100 KV/S" is valid. If for example the JQE–HS is programmed for a six volt (p-p) sine wave output, the frequency response (3 db point) will be 5.5 kHz.

2-3 AC STABILITY ADJUSTMENTS

2-4 The relatively wide bandwidth of an operational power supply may give rise to "ringing", "overshoot", or "oscillations" at the output, especially at high programming speeds and when operating into loads not purely resistive. In the JQE–HS models, provisions have been made for stabilizing the amplifier for a limited range of load conditions. A lag network at the input (LAG I) a variable feedback capacitor (C_f) and a lag network at the output of the amplifier (LAG II) may be individually adjusted for various load conditions (See FIG. 2-1 for the location of these internal controls).

2-5 The AC characteristics of the JQE–HS are optimized at the factory by programming the input with a square wave, while operating into a resistor load. The supply is fully loaded and a gain-ratio of unity is used (worst condition). The factory procedure may be readily duplicated, using a test set-up as shown in FIG. 2-1 below.

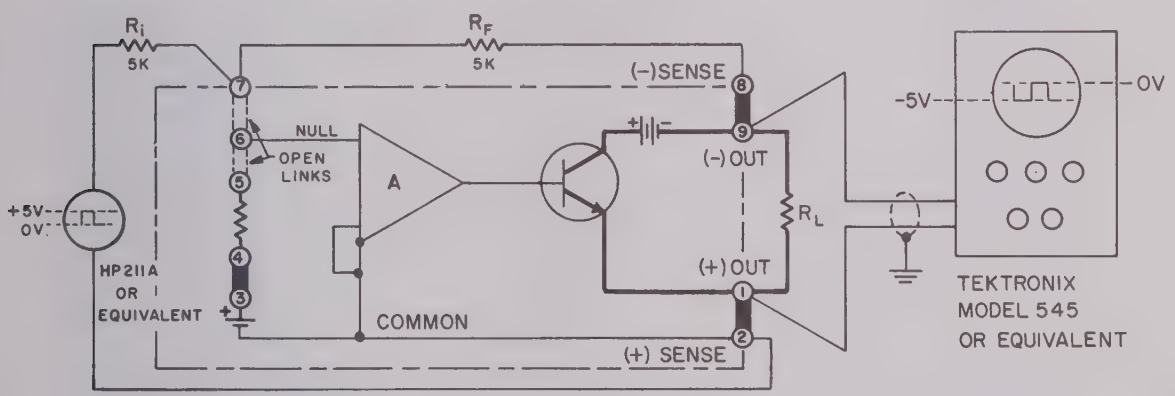


FIG. 2-1 SET–UP FOR AC STABILITY ADJUSTMENTS

2-6 Turn JQE–HS "on" and observe the resulting output wave form on a suitable oscilloscope (Bandwidth > 10 MHz.) Adjust the lag networks and the feedback capacitor for optimum wave shape (Refer to FIG. 2-2).

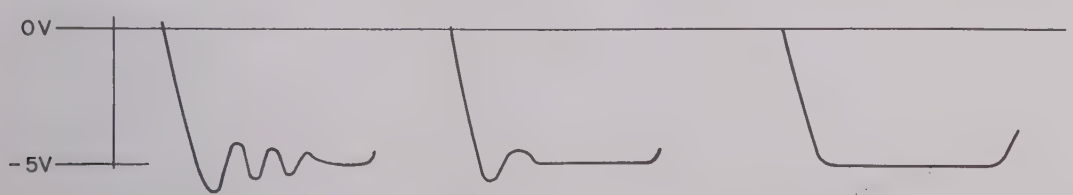


FIG. 2-2 SQUARE WAVE RESPONSE

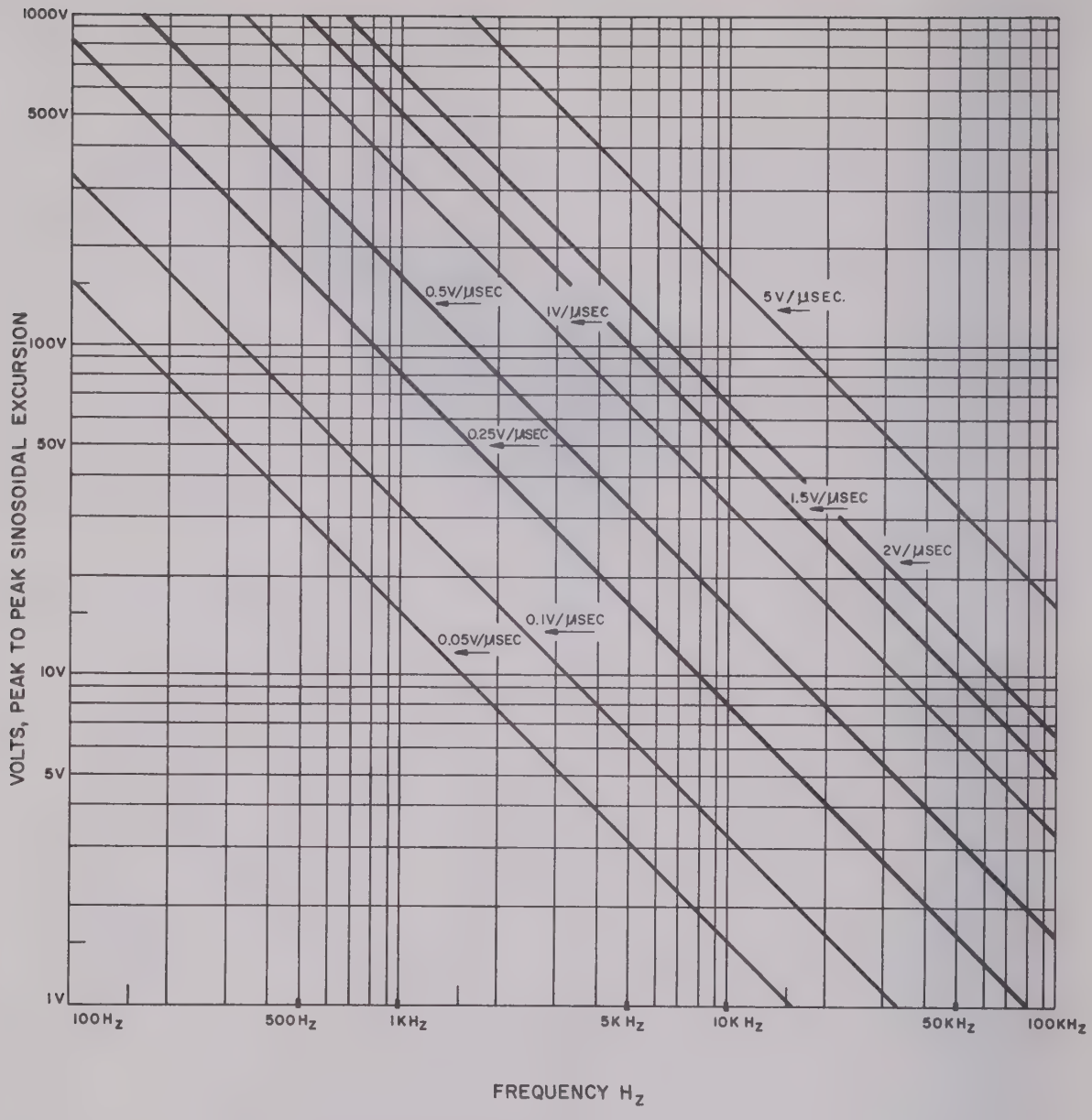


FIG. 2-3 OUTPUT (p-p) VS. SINE WAVE FREQUENCY

2-7 The standard adjustment for optimum amplifier square-wave repsonse as described above may not be suitable for all applications and load conditions. In these cases, the load should be monitored with a high-speed oscilloscope as before, and the lag networks adjusted to the response desired.

2-8 CAPACITIVE LOADING

2-9 JQE High speed power supplies can drive loads having a maximum capacity to 0.001 μF. If loads with larger capacitive content must be driven, several techniques can be used, including the addition of external capacity across the feedback terminals (6)-(8) on the rear barrier strip (TB1). Another useful technique is the use of isolation resistance in series with the load. The latter method may be used with two variations:

a) SERIES RESISTANCE EXTERNAL TO FEEDBACK LOOP.
The advantage of this method is that the network equations are not affected. The disadvantage is the rise in output impedance, evidenced by degraded load regulation (Refer to FIG. 2-4).

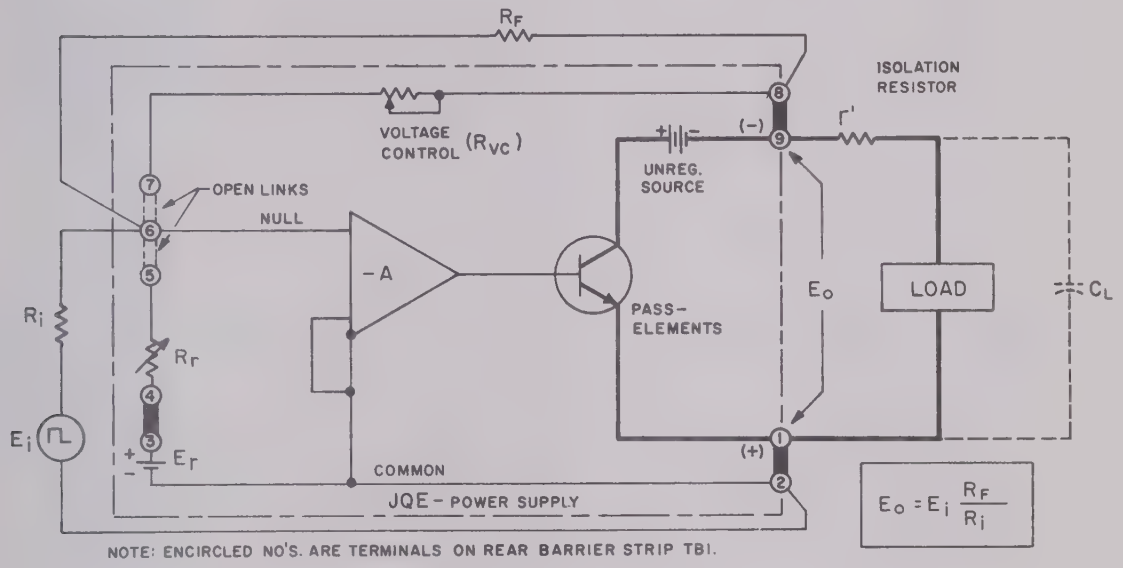


FIG. 2-4 ISOLATION RESISTOR EXTERNAL TO FEEDBACK LOOP

b) SERIES RESISTANCE PART OF FEEDBACK LOOP. The advantage of this method is that the isolation resistance (r') will not affect the output impedance. The disadvantage is that r' must be considered part of the feedback resistance (R_f) and will modify the transfer function thus:

$$E_o = -E_i \frac{R_f + r'}{R_i}$$ (Refer to FIG. 2-5)

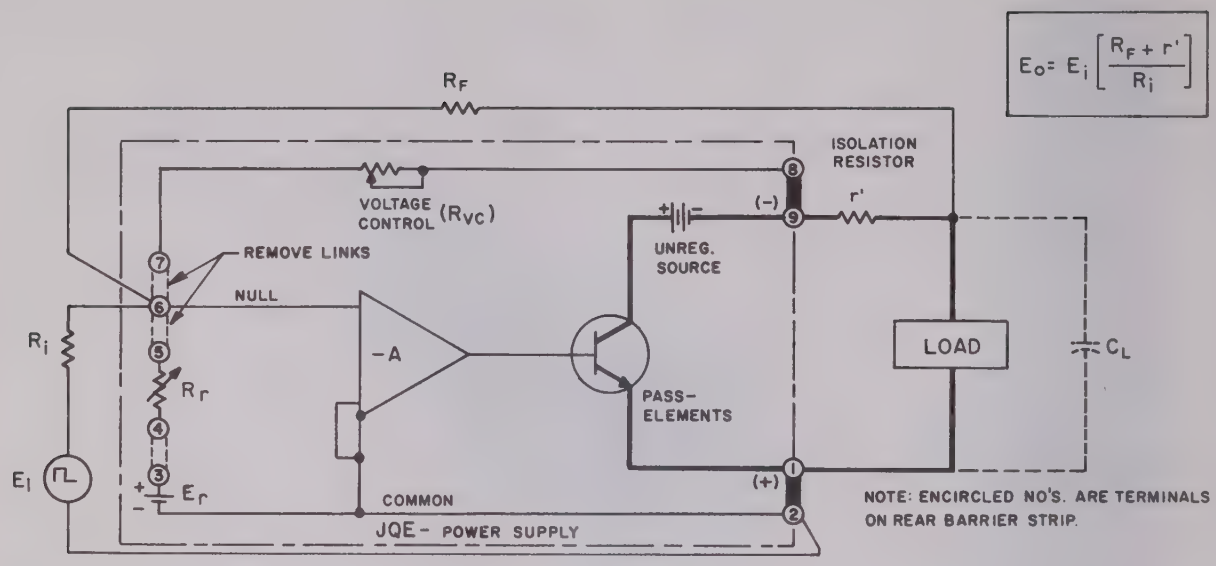


FIG. 2-5 ISOLATION RESISTOR PART OF FEEDBACK LOOP

2-10 The isolation resistor (r') should be selected as small as possible since the voltage drop across it is lost and subtracts from the available output voltage. The best method of compensating for load capacitance must be chosen in view of the desired advantages as described above. Most likely, it will be a combination of lag network adjustment, additional feedback capacitance and/or isolation resistance. In general, it is best to avoid excessive load capacitance as much as possible, since any compensating method has the disadvantage of narrowing the bandwidth and thus reduce the programming speed of the operational power supply.

SECTION III – MEASUREMENTS

3-1 GENERAL

3-2 The procedures and diagrams presented in Section V (paragraph 5-16) of the standard manual are equally applicable for the high speed version of the JQE power supplies. The supplementary offset specifications given for the JQE–HS models and the slewing rate, may be measured by following the procedures outlined in the following paragraphs.

3-3 OFFSET SPECIFICATIONS, DEFINITIONS

3-4 GENERAL. Variations in DC output voltage of a power supply due to AC line changes or DC load are mainly the indirect result of errors or "offsets" at the amplifier inputs. The offsets are transferred to the output of the power supply, multiplied by the instantaneous value of the closed loop gain and the feedback resistor at the point of operation. Since a standard power supply has a very limited range of gain and feedback resistance, changes due to AC line, DC load, temperature and time may be directly referred to the output of the power supply.

3-5 In high speed operational power supplies however, where the closed loop gain and the value of the feedback elements are selected by the user (and may therefore vary over a much wider range) offset specifications are refered to the input of the amplifier. Once the user has selected the feedback components and the closed loop gain, DC output variations can be calculated for each individual case. In order to familiarize the user with the offset concept, a brief review of operational theory is presented in the following paragraphs.

3-6 OPERATIONAL POWER SUPPLIES. For many years, "Operational Amplifiers" have been used in the field of analog computing to perform various mathematical "operations". An operational amplifier is a high gain, DC coupled amplifier, stabilized externally by negative feedback to exhibit gain characteristics depending only on the external feedback components.

3-7 Since a regulated power supply contains such an amplifier in a closed loop configuration and in series with a power amplifier stage (consisting of the driver stage and the series regulator) it can be analyzed in terms of operational terminology. In fact the power supply becomes a high power amplifier if the input terminals are made accessible. To complete the conversion, the bandwidth restricting output capacitor must be removed and the amplifier stabilized. The result is an "Operational Power Supply", programmable like an operational amplifier, with much higher power but with single ended output.

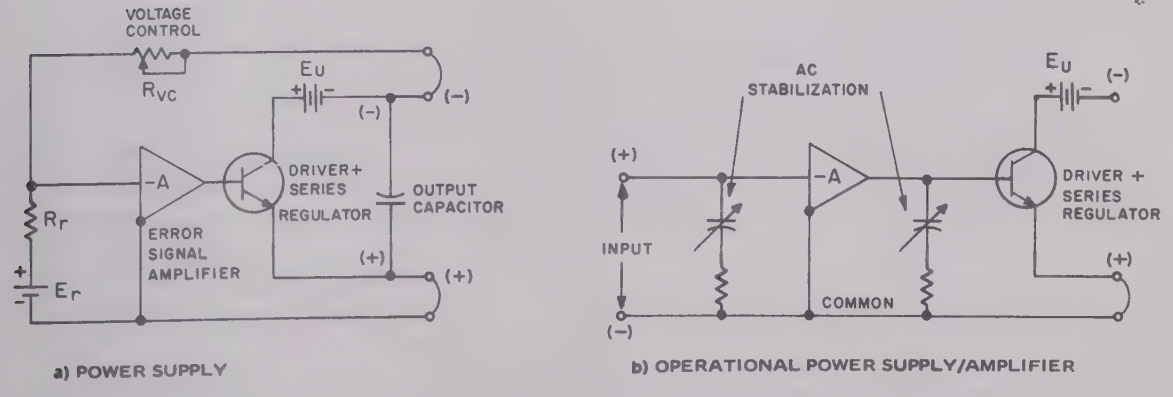


FIG. 3-1 POWER SUPPLY–AMPLIFIER CONVERSION

3-8 For the purpose of analysis, FIG. 3-1b may be further simplified by including the driver/pass element stage and the unregulated source (E_u) into the amplifier triangle. Adding an input source (E_i) with series resistor (R_i) and a feedback resistor (R_f), a model of an "ideal" operational amplifier results, for which the following assumptions are made:

1) The input terminal (null junction) will not conduct current to or from the amplifier.
2) The open loop gain is very large ($A \to \infty$)
3) The input voltage ϵ approaches zero when the feedback loop is closed.

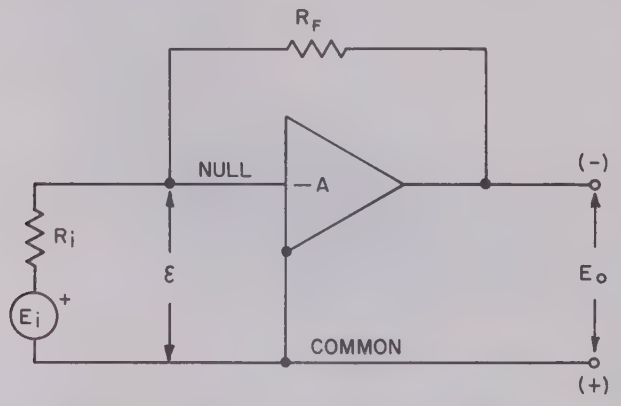


FIG. 3-2 MODEL OF IDEAL OPERATIONAL AMPLIFIER WITH FEEDBACK (INVERTING CONFIGURATION)

3-9 Refer to FIG. 3-2. Since (by the previous assumptions) the currents in R_f and R_i are equal, Kirchhoff's loop equation may be written as:

$$\frac{E_i - \epsilon}{R_i} + \frac{E_o + \epsilon}{R_f} = 0 \quad (1)$$

By definition, $E_o = -A\epsilon$, where A represents the open loop gain. Also, $\epsilon = \frac{E_o}{A}$.

If we let $A \to \infty$, $\epsilon \to 0$ then (1) becomes $\frac{E_i}{R_i} + \frac{E_o}{R_f} = 0$ or in the more familiar form,

$E_o = -E_i \frac{R_f}{R_i}$ (2), where $\frac{R_f}{R_i}$ represents the closed loop gain G.

Equation (2) represents the transfer function of the operational amplifier in the inverting configuration.

3-10 THE PRACTICAL AMPLIFIER. The closed loop relationships derived for the ideal model of the operational amplifier may be applied to the practical case with very small predictable errors which in addition can be compensated for. The "ideal" amplifier equation (2) for example may be used to calculate the magnitude of the feedback and input components with negligible errors which will be well within the component tolerances. In evaluating the DC performance however, these errors can not be neglected.

3-11 AMPLIFIER OFFSETS. In developing the ideal amplifier model, we tacitly assumed the null junction voltage and current to be zero, causing the "ideal" equation (2) to be valid. In reality however, nonidealities do exist at the null junction or input terminal of the amplifier. These errors are of the form of an offset voltage (E_{io}) and an offset current (I_{io}). The modified version of the amplifier model can be shown with these error sources as in FIG. 3-3.

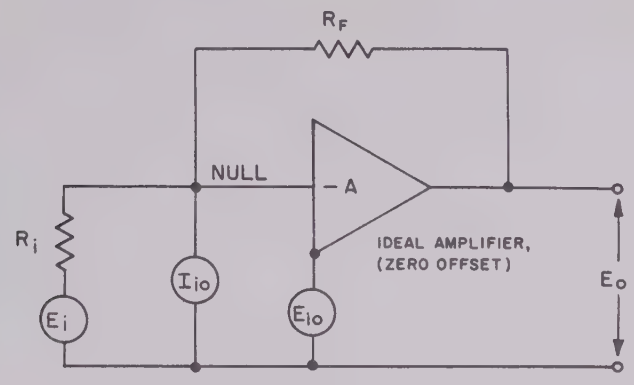


FIG. 3-3 AMPLIFIER WITH INPUT OFFSET SOURCES

3-12 Although the INITIAL values of both offsets (E_{io},I_{io}) may be cancelled by compensating sources of opposite polarity, the VARIATIONS of the offsets (ΔE_{io},ΔI_{io}) with AC line, DC load, temperature and time do exist at the amplifier input and are specified as shown in the table (See paragraph 1-5).

3-13 Considering both offset variations, the modified expression for the output voltage (with the initial or fixed parts of E_{io} and I_{io} nulled) becomes:

$$\underbrace{E_o = E_r \frac{R_f}{R_i}}_{\text{Ideal Part}} + \underbrace{\Delta E_{io}\left[\frac{R_f}{R_i} + 1\right]}_{\substack{\text{Output effect,}\\ \text{offset voltage}\\ \text{change}}} + \underbrace{\Delta I_{io}\, R_f}_{\substack{\text{Output effect,}\\ \text{offset current}\\ \text{change}}} \qquad (3)^*$$

As seen from equation (3), the offset voltage variations (ΔE_{io}) are multiplied by unity, plus the gain ratio R_f/R_i to obtain its contribution to the output voltage change, while the offset current variations are multiplied by the value of the feedback resistor (R_f). The separation of the specifications for ΔE_{io} and ΔI_{io} not only allows an analysis of the contributions from each offset variation, but also permits component selection such as to minimize the effects of either offsets or find an acceptable compromise.

3-14 A practical example will illustrate the usefulness of the concept: For an application requiring a gain of 10, an operational programming circuit is to be set up with a Model JQE 100—1(M)—HS. We are interested how in this case an AC line variation from 105V AC to 125V AC will affect the output. From the table of specifications, the line regulation is found to be **(worst case)**:

E_{io} = 10 μV (max.)
I_{io} = 2 nA (max.)

*The expression does not include second order effects such variations due to changes in the reference voltage (E_r), the feedback resistor (R_f) and the input resistor (R_i).

If we let R_i = 100K ohms, then for R_f/R_i = 10, we need R_f = 1 megohm. The expected output variation due to AC line changes are (from Eq. 3):

$$\Sigma \Delta E_o = \Delta E_{io}\left[\frac{R_f}{R_i} + 1\right] + \Delta I_{io} R_f$$

$$\underbrace{= 10\,\mu V\,(10+1) = 110\,\mu V}_{\Delta E_o \text{ due to } \Delta E_{io}} \quad + \quad \underbrace{2\text{ nA } (10^6 \text{ ohms}) + 2\text{ mV}}_{\Delta E_o \text{ due to } \Delta I_{io}}$$

The contribution of ΔI_{io} are seen to be almost 20 times that of ΔE_{io}, due, of course, to the choice of a relatively large feedback resistor. Generally, in low gain applications, it is wise to select lower values for the feedback and input resistors if the loading on the input source will permit it; if for example: R_i = 10 K and R_f = 100 K (retaining the previous gain ratio of $R_f R_i$ = 10), ΔE_o would only be .11 mV + 0.2 mV = .0.33 mV, calculated similar to the previous results. Using the same amplifier in an application with a required operational gain of 100, we select R_i to be 1 K ohm and R_f = 100 K, thus establishing the desired gain ratio (R_f/R_i = 100). For this choice of input and feedback elements, we find the output variation to be:

$$\Delta E_o = \underbrace{10\,\mu V\,(100+1) = 1.01\text{ mV}}_{\Delta E_o \text{ due to } \Delta E_{io}} \quad + \quad \underbrace{2\text{ nA } (1\text{x } 10^5 \text{ ohms}) + 0.2\text{ mV}}_{\Delta E_o \text{ due to } \Delta I_{io}}$$

In this case the contribution from ΔE_{io} is dominant. Its effect can only be reduced by externally limiting wide variations of the AC line voltage. Similar calculations to the above examples may be made to predict the effects of load changes, temperature variations or stability (changes in output voltage with time).

3-15 OFFSET ADJUSTMENT AND MEASUREMENT

3-16 The initial amplifier offsets (E_{io},I_{io}) should be nulled before each programming application. Accurate results using the simplified transfer function (Eq.2) can be expected only if the initial offsets are close to zero and if close tolerance feedback elements (R_i,R_f) are used. While the "E_{io} null" control is a built-in feature in all JQE–HS supplies, for precision programming an external "I_{io} null" control may be connected as shown in FIG. 3-4 below.

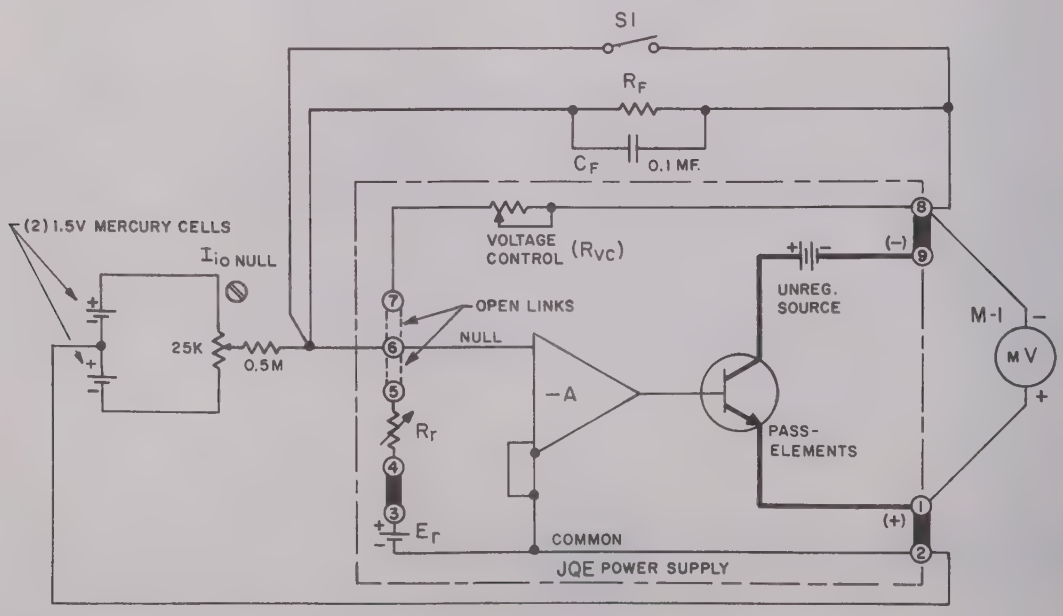


FIG. 3-4 TEST SET-UP FOR OFFSET NULLING AND ΔE_{io}, ΔI_{io} MEASUREMENTS

3-17 PROCEDURE

a) Connect external components as shown in FIG. 3-4. The feedback resistor (R_f) can be a decade box, a fixed external resistor or the internal voltage control can be used (by closing the link between terminals 6-7). External components used in the test set-up should be of high quality, the resistors should have temperature coefficients of 20 parts per million or better, the capacitor (C_f) should be a mylar or polystrene type. The milli-voltmeter at the output (M1) should be a sensitive null detecting instrument such as the Millivac Model MV–07C, the Keithley Model 150B or an equivalent instrument. S1 is a single pole switch, connected directly to terminals (6)-(8) for shorting-out R_f.

B) E_{io} NULLING. Turn both, JQE-HS and NULL-Detector on and allow for sufficient warm up time. Locate the "E_{io} null" control (See FIG. 1-1 in this addendum). With S1 closed, adjust "E_{io} null" control until zero volts are read-out on M1.

c) The adjustment may shift slightly as the power supply and the Null Detector are reaching their thermal equilibrium. Repeat adjustment as necessary.

d) ΔE_{io} Measurement. Varying the AC line input to the JQE–HS by means of a suitable variable transformer or line-step device, the variations of the offset voltage (ΔE_{io}) versus AC line can be measured.

e) I_{io} NULLING. Open S1. Set external "I_{io} null" control until zero volts are read-out on M-1. The sensitivity of the adjustment becomes greater as the feedback resistance (R_f) is increased. With careful adjustment, the offset current can be reduced to a few nano-amperes (1 nano ampere across R_f = 1 M ohm represents an output voltage of 1 mV as ead-out on M-1). Offset current nulling results in raising the input impedance looking into the feedback terminals (6-8), important for applications where the JQE–HS is used for impedance transformation. The input impedance (Z_{in}) in this case will be directly proportional to the residual offset current. With careful adjustment, Z_{in} can be raised to $10^7 - 10^8$ ohms.

f) ΔI_{io} Measurement. Varying the AC line between the specified limits as described before, the variations of the offset current (ΔI_{io}) versus AC line can be verified.

3-18 The above described measurements provide an excellent and simple means of checking the DC performance of the high speed power supply. Measuring offset variations (ΔE_{io}, ΔI_{io}) while changing the load, temperature or over a long time period requires specialized test equipment and a controlled environment, not readily available in the laboratory. Information concerning these tests may however be obtained from the factory.

3-19 PROGRAMMING SPEED MEASUREMENTS

3-20 "Programming Speed" or "Slewing Rate" refers to the rate of change of the output voltage (E_O) following a step-change in programming signal (amplifier connection), a change in feedback resistance (high-speed resistance programming) or a change in load resistance (current regulator connection). Whatever the source of the step change, the power supply will respond with an exponential waveform, the rise and fall-times of which depending upon the over-all output capacity of supply and load.

3-21 The Programming Speed (Slewing Rate) as specified by Kepco, is measured under the following conditions:

a) Gain ratio 1:1

b) Supply fully loaded (Resistor Load)

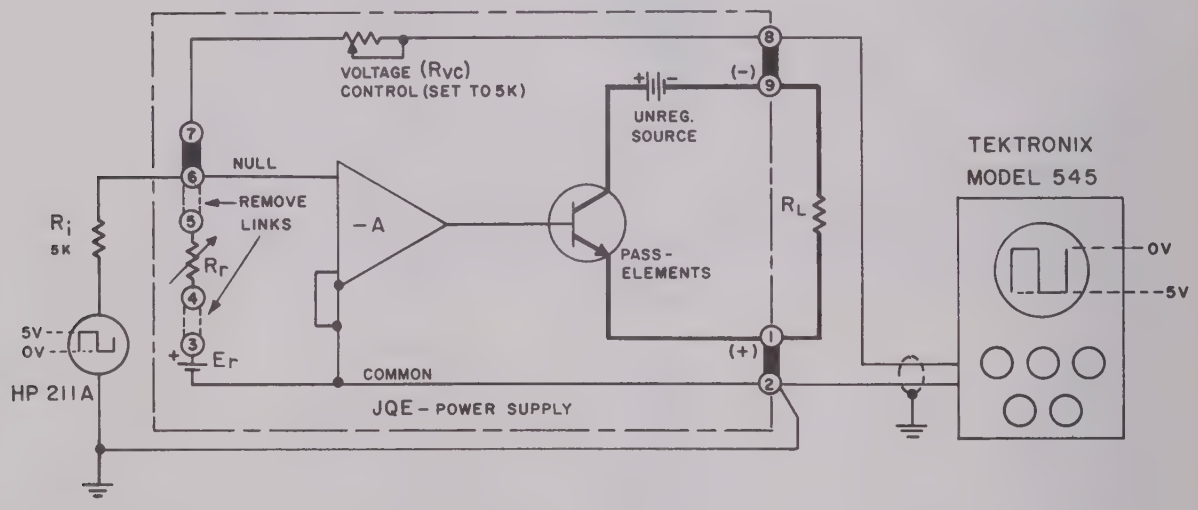
c) Input signal: 100 Hz square wave with a rise time at least one order of magnitude faster than the specified slewing rate of the power supply.

d) Rise time and fall time of the output signal is measured from E_O = 0V (or E_O = maximum) to one time constant (63% E_O).

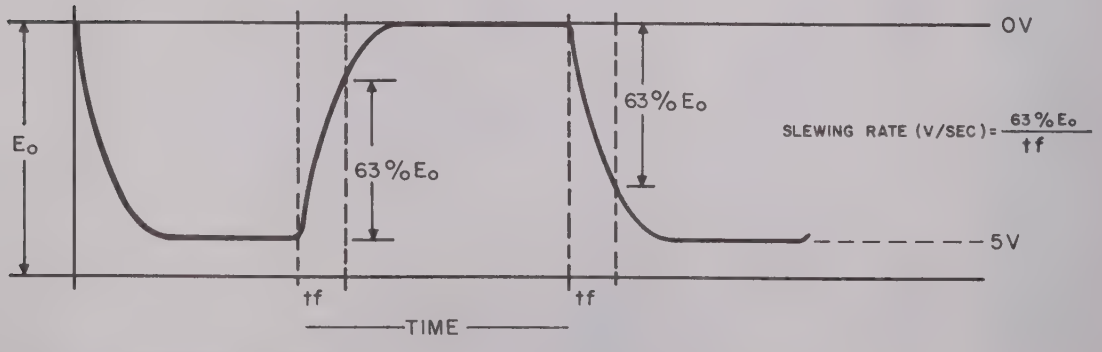
e) The test set up for programming speed (slewing rate) measurements is shown in FIG. 3-5. The Programming speed (slewing-rate) is calculated by:

$$\text{Programming Speed (Slewing Rate)} = \frac{63\%\ E_O}{t_r \text{ or } t_f}$$

where t_r or t_f is the rise or fall time (whichever is slower) of the exponential output wave form.



a) TEST SET UP



b) OUTPUT WAVE—FORM

FIG. 3-5 TEST SET-UP (a) AND OUTPUT WAVE FORMS (b) FOR PROGRAMMING SPEED (SLEWING RATE) MEASUREMENTS

3-22 SINUSOIDAL FREQUENCY RESPONSE. The graph in Section I of this addendum (Refer to FIG. 2-3) may be used to determine at a glance the approximate full power sine wave response of the high speed power supply. Since the programming speed (slewing rate) of the JQE–HS may however be higher than the specified minimum, the latter may first be determined by measurement as described previously (See paragraph 3-19) and the sine wave response calculated as demonstrated below.

3-23 If the input voltage is of the form:

$$e = E_{max.} \sin \omega t,$$

its slope at any point is given by the derivative with respect to time:

$$\frac{de}{dt} = \omega E_{max.} \cos \omega t$$

The maximum slope of a sinusoidal occurs at the point of crossover of the zero axis, namely where $\omega t = \pi$. Evaluating the derivative at this point, we have

$$\left| \omega E_{max.} \cos \omega t \right|_{\pi} = -\omega E_{max.}$$

Since the programming speed (slewing rate) is defined as the change in output voltage with respect to time

$$\frac{\Delta E_o}{\Delta t}$$ we can equate:

$$\text{Programming Speed (Slewing Rate)} = \frac{\Delta E_o}{\Delta t} = -\omega E_{max.} = -2\pi f E_{max.}$$

The maximum sinusoidal frequency is therefore:

$$f_{max.} = \frac{\text{Programming Speed (Slewing Rate)}}{2\pi E_{max.}}$$

If for example a Model JQE 100–1M–HS is to sine wave programmed to full output, and the programming speed has been measured to be 138,000 volts/second the maximum sine wave frequency which can be programmed is calculated to be:

$$f_{max.} = \frac{138{,}000}{\pi(100)} = 440 \text{ Hz.}$$

If the programming frequency exceeds 440 Hz, slope distortion and clipping will occur.

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INSTRUCTION MANUAL CORRECTION

Please note the following corrections to the manual material as indicated:

Parts List and Schematic Diagram:

1.	CHANGE:	R2	Resistor,Fixed,Molded	27 ohm, 1/4 W, 10%	Kepco No. 115-2317 to:
		R2	Resistor,Fixed,Molded	10 ohm, 1/4 W, 10%	Kepco No. 115-2230.
2.	CHANGE:	Q3,4	Transistor,Silicon,NPN		Kepco No. 119-0056 to:
		Q3,4	Transistor,Silicon,NPN		Kepco No. 119-0093.
3.	CHANGE:	C201	Capacitor,Electrolytic,Can	10.4 K μF, 75V	Kepco No. 117-0628 to:
		C201	Capacitor,Electrolytic,Can	10.4 K μF, 75V	Kepco No. 117-0722.
4.	ADD:	C16	Capacitor,Ceramic,Disc.	0.005 μF, 200V, 10%	Kepco No. 117-0061.
5.	ADD:	R28	Resistor,Fixed,Molded	560 ohm, 1/4 W, 10%	Kepco No. 115-2210.
6.	ADD:	C17	Capacitor,Mylar,Pigtail	0.05 μF, 200V, 10%	Kepco No. 117-0362.
7.	ADD:	C301,302	Capacitor,Ceramic,Disc.	0.05 μF, 500V	Kepco No. 117-0163.

An input network consisting of C16 and R28 has been added on Assembly A1, in series with NULL junction (inverting input) of the programming amplifier (A_V).

A bypass capacitor (C17) has been added on Assembly A1 from the base of Q4 to the amplifier "common" (TB1–2).

Bypass capacitors (C301, C302) have been added to Assembly A3, in parallel with CR301 and CR302 respectively.

ERRATA:

1. Error in parts list page 6-5/6-6:
   R15, Kepco No. 115-0520 should read: "R15, Kepco No. 115-0502."

8.	CHANGE:	Q2	Transistor,Silicon,NPN		Kepco No. 119-0094 to:
		Q2	Transistor,Silicon,NPN		Kepco No. 119-0059.
9.	DELETE:	R9	Resistor,Fixed,Molded	1 K ohm, 1/2 W, 5%	Kepco No. 115-0340.
10.	ADD:	C18	Capacitor,Ceramic,Disc.	0.005 MF, 500V	Kepco No. 117-0061.

NOTE: C18 has been added to Assembly A1. It is connected between the collector and the emitter of Q2.

JQE 36–3(M)HS/10-1569/r3	C2295
JQE 36–3(M)HS/3-970/r3	BMC
JQE 36–3(M)HS/4-370/r3	BMC
JQE 36–3(M)HS/4-2270/r3	C2395
JQE 36–3(M)HS/5-570/r3	BMC
JQE 36–3(M)HS/8-2671/r4	C2706
JQE 36–3(M)HS/3-1072/r5	C2841
JQE 36–3(M)HS/8-2272/r6	C2958

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